

REGISTRATION FORM

Five Days National Level Workshop

On

“DSP and Image Processing through Machine Learning”

08th -12th January 2019

1. Name. _____

2. Designation. _____

3. Department. _____

4. Postal Address. _____

5. Phone No. _____

6. Email-ID. _____

Registration Fee Details :

Transaction No :

Date :

Signature of the Applicant

Mr./Ms./Dr. _____ is an employee of our Institute and is hereby permitted for the above Workshop.

Date :

Head of the Institution

Place :

CHIEF PATRONS

Sri A S Vishwanatha, President NES

Sri T R Ashwathnarayana Setty, Vice President NES

Sri S N Nagaraja, Secretary NES

Sri Amarendra Kireeti, Joint Secretary NES

Sri C R Nagaraj, Treasurer, NES

PATRONS

Dr. H R Mahadevaswamy, Principal JNNCE, Shimoga

Dr. L K Sreepathi, Vice Principal JNNCE, Shimoga

CONVENER

Dr. Manjunatha P, HOD, Dept. of ECE, JNNCE

CO-CONVENERS

Mrs. Roopa B.S, Asst. Professor Dept. of ECE, JNNCE

Mrs. Prema K.N, Asst. Professor Dept. of ECE, JNNCE

Mrs. Smitha S.M., Asst. Professor Dept. of ECE, JNNCE

ORGANIZING COMMITTEE

All Teaching and non Teaching staff of Electronics & Communication Engineering Department

ADVISORY COMMITTEE

Dr. Ashok Rao

Former Head, Network Project, CEDIT, IISc, Bangalore

Dr. T N Nagabhushan

Principal and Professor, SJCE, Mysore

Dr. B G Sangameshawara

Vice Chancellor, JSS Science and Technological University, Mysore

Dr. K N Subramanya

Principal and Professor, RVCE, Bangalore

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NES Education Society (R)

Jawaharlal Nehru National College of Engineering, Shimoga - 577 204



[Affiliated to VTU Belagavi, Recognized by Govt. of Karnataka, Accredited by NAAC and Approved by AICTE, New Delhi]

Five Days National Level Workshop

On

“DSP and Image Processing through Machine Learning”

In Association with ISTE & IETE



From : 8th to 12th January- 2019

Organized by

Department of Electronics & Communication Engineering



ABOUT THE COLLEGE

JNNCE was established in the year 1980 by the National Education Society. The college is affiliated to Visvesvaraya Technological University (VTU), Belgaum and recognized by AICTE, New Delhi. Institution is NAAC accredited and UGC 2f and 12B certified. It offers 7 UG and 7 M.Tech program besides MBA and MCA. It has 7 Research Centers recognized by VTU. Spread over 52 acres, the campus has all the amenities to cater to the needs of about 3500 students and 400 faculty and staff. All the departments have good infrastructural facilities with highly qualified and experienced faculty, staff and well equipped laboratories. The college is regarded as one of the excellent centers for technical education and research. JNNCE is ranked 42nd among top Engineering colleges in India and 7th in Karnataka as per Outlook survey 2018. The college is providing its fruitful services in the 38th year. The college organizes international and national workshops, conferences, seminars and short term courses on a regular basis. The institution has IEEE as well as ISTE members.

ABOUT THE DEPARTMENT

The Department of Electronics & Communication Engineering was started in the year 1980. Department have well equipped laboratories, qualified and experienced staff to cater to the needs of students of UG program and PG program M.Tech in Digital Electronics and Communication Systems. The ECE department has been recognized as research center under which 16 research scholars are pursuing Ph.D under VTU Belgaum. UG and PG students are actively involved in carrying out their projects at New Age Incubation Network. The department has IETE and ISF forums. The Institute of Electronics and Telecommunication Engineers (IETE) center is the national apex body of professional engineers devoted to the advancements of Electronics, Telecommunications, IT and allied areas. The 56th center of IETE was inaugurated at Jawaharlal Nehru national College of Engineering Shivamogga on 1st March 2009. IETE serves more than 69,000 members both individual and industrial organizations through its 65 centers spread all over India including one abroad.

DEPARTMENT VISION

To be a premier centre of education and research in Electronics & Communication Engineering for producing competent engineers to cater to the needs of industry and society.

DEPARTMENT MISSION

- * Impart quality education through innovative and continuous teaching – learning process
- * Groom the students with required foundations of Electronics & Communication Engineering and inculcate in them the professional ethics & human values.
- * Train the students and the faculty in state – of – the-art technologies and motivate them for higher studies & innovation based research for the needs of the industry and society.
- * Create centers of excellence in the field of Electronics & Communication Engineering in collaboration with the industries and academic organizations.

ABOUT THE WORKSHOP

This workshop is being organized for Five days to provide a platform for faculties, researchers and technologists, to understand the concepts of Pattern Processing and machine learning to solve complex engineering problems. With this in view the course would devote time talks on the topics cited below along with 'Hands-on Sessions'.

TOPICS

- * Introduction to Pattern Processing, Machine Learning, Mathematics of Machine Learning
- * Advanced Algorithms for Machine Learning
- * Biomedical Applications using Machine Learning
- * CNN, Deep Learning techniques and Applications
- * Laboratory sessions using Python, R & MATLAB

PARTICIPATION

The interested academicians from Electronics, Telecommunication, Computer science, Information science, industry personnel, researchers and allied disciplines are invited to attend the five days Workshop.

RESOURCE PERSONS

Resource persons from distinguished Research Institutes like IISC, NITK, CMI, industries and colleges will deliver expert lectures and practical sessions

REGISTRATION DETAILS

A registration fee of Rs. 1000 will be charged for each participant. The registration fee should be paid through online, the details of which are given below.

Bank	:	CANARA BANK
Branch	:	JNNCE Campus, Shimoga
SB A/c. No.	:	6322101000028
IFSC code	:	CNRB0006322

Scanned copy of the application with dully forwarded by the HOD and Principal, must be sent along with online transaction to the conveners email id. The number of participants is limited to 45 on first come first serve basis.

Note : Registration fee does not include accommodation. However accommodation may be provided to the participant with a nominal fee in the college hostel. No TA/DA will be provided for the participants.

IMPORTANT DATES

Submission of application forms by :
04th January 2019.
Workshop Dates : 8th to 12th January -2019.

VENUE :

Seminar hall (Ad-Block), Computer Lab (ECE Dept.)
Timings: 9.00 am to 5.30 pm