



1	Name of the Teaching Faculty	Abhijith. N
2	Designation	Assistant professor
3	Email ID	abhijithn@jnnce.ac.in
4	Date of Joining the Institution	05/08/2009
5	Qualification	M.Tech,
6	Total Experience (Years)	Teaching Industry Research 08 0 0
7	Ph.D / Research Area	Not Applicable
8	Journals and Conference Papers Published details	
	1. Design and Implementation of First two stages of HFB ADC using Cadence Virtuoso tool"- International Journal of Engineering Research & Technology (IJERT) ISSN: 2278-0181, Vol 4, Issue, July-2015	
9	Areas of Additional Technical knowledge acquired apart from the regular teaching subjects	

10	<p>Conferences attended details-year wise</p> <p>1. Attended International joint colloquium on “Emerging Technologies in Computer, Electrical and Mechanical, CEM 2011” held on December 27-28, 2011 in JNNCE, Shimoga</p>
11	<p>Books Published / IPRs/ Patents</p> <p style="text-align: center;">NIL</p>
12	<p>Professional Memberships</p> <p style="text-align: center;">MISTE,MIETE</p>
13	<p>Consultancy Activities</p> <p style="text-align: center;">NIL</p>
14	<p>Grants fetched details</p> <p style="text-align: center;">NIL</p>
15	<p>Achievements and Recognitions / Awards</p> <p>1. Participated as a chair person for National level symposium on Advanced Engineering and Technology at SJMIT Chitradurga on 29/4/2016.</p>