


Name	K V CHAITRA	
Designation	Asst Professor	
Qualification	B.E, M.Tech (CNE)	
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Experience		
1. Teaching	1 Year	
STTP/Workshops/Conferences attended	<ol style="list-style-type: none"> 1 Day workshop on “ARM Processor & Applications “ held at GMIT , Davanagere 1 week workshop on National Board of Education and Outcome based Education held at Shimoga Two days workshop on “Big Data Analytics using Tableau” conducted by Dept of Information Science and Engineering, at AMC Engineering College 2 week ISTE STTP workshop on CMOS, Mixed Signal and Radio Frequency VLSI Design held at JNNCE, Shimoga 	
Research Publications		
• Journals	<ol style="list-style-type: none"> 1. Published a paper “A Reverse Tracing Scheme and Efficient Key Management For Secured Communication In MANET” in IJERT Conference proceedings, Volume 3, Issue 19 	
• Conferences	<ol style="list-style-type: none"> 1. Presented a paper “A Reverse Tracing Scheme and Efficient Key Management For Secured Communication In MANET” in the “International Conference on Engineering, Science, Management and Advances in Research Technology (ICESMART-15)” held at T. John Institute of Technology. 2. Presented a paper “Enhancement of Data Security in MANET with Two Level Encryption” in the 2nd International Conference on Advances in Computer and Communication Engineering (ACCE-2015) held at Vemanna Institute of Technology and won the best paper award 	

3. Presented a paper "**A 2ACK with PFC Scheme to Detect Misbehavior Nodes In MANET**" in the National Conference on Real-Time Systems held at City Engineering College
4. Presented a paper "**An ARM System To Detect Selfish Nodes in MANET with DHT**" in the 3rd International Conference on "Computer Networking and Information Processing (ICCNIP-2015)" held at AMC Engineering College