


**JAWAHARLAL NEHRU NATIONAL COLLEGE OF ENGINEERING
SHIMOGA**

Faculty Bio-Data

1	Name of the Teaching Faculty	Harisha S B		
2	Designation Contact Details: Mobile No: Email Id:	Assistant professor Dept.of TCE JNNCE,Shimoga 9686214454 harishasb@jnnce.ac.in		
3	Department	Telecommunication Engineering		
4	Date of Joining the Institution	01-08-2003		
5	Qualification with % of Marks & Grade with year	UG BE, FCD, 2003 JNNCE Shimoga	PG M.Tech, FCD, 2009 NIT Warangal	
6	Total Teaching Experience at JNNCE and at other places	Teaching JNNCE:13yeras 10 months	Industry Nil	
7	PhD Guide? Give field & University No			
8	Journals and Conference Papers Published details Year:2005 1. Jagadeesha.S.N, Harisha S.B., Chetan K.R., “A novel Graphical user interface for fuzzy queries”, National Conference on “Vision, Graphics & Image Processing”, 2 –3 March , 2005 J.N.N. College of Engg., Shimoga, Karnataka 2. Jagadeesha.S.N, Harisha S.B., Chetan K.R, “Steganography for detection of image tampering” 5th National Workshop on Cryptology, 12- 14, August, 2005, JNNCE, Shimoga 3. Jagadeesha.S.N, Harisha S.B., Chetan K.R, “Localizing the effects of image tampering using information hiding” National Workshop on Information Security, 25- 26 November, 2005, Karunya Institute of Technology and Sciences, Coimbatore			

4.

Year:2006

5. Harisha S.B., Shreedhar A Joshi., Madhukar C.S., “FPGA’s for design of custom digital signal processing devices” National Conference on Signal processing and communication, 07- 08, July, 2006, JNNCE, Shimoga
6. Chetan K.R., Harisha S.B., A Compression Resistant and High Capacity Watermarking for Digital Images, National Conference on COMPUTING & COMMUNICATION, Dr. Mahalingam College of Engg., & Technology, Pollachi, Tamilnadu.,15-16th September, 2006, pp.23-26
7. Chetan K.R.,Harisha S.B., Protecting ROI of the satellite images using compression based colour double watermarking, First International conference on signal and image processing , BVB college of engineering and technology, Hubli.,7-9 December-2006, pp 52 -56

Year:2007

8. Chetan K.R., Harisha S.B., Pradeep H K., A watermarking scheme for protecting cartoon images, National Conference on VLSI design signal processing and communication, NCVSPCOM -2007, Dept of ECE, Siddaganga institute of technology, Tumkur, 17-18 February 2007, pp. 102– 106
9. Chetan K.R., Harisha S.B., Pradeep H K., Secured colour double water marking for satellite images, National conference on knowledge based computing system and frontier system, NCKBFT – 07, Dept of CSE, MIT, Manipal,19-20 February 2007.

Year:2010

10. Chetan K.R., Harisha S.B., A Reversible Watermarking Scheme For Protecting Medical Images, National Conference on Intelligent Information Retrieval, Dept. of Information Technology, PSG College of Technology, Coimbatore, 24-25 September 2010.

Year: 2013

11. Ranjith Ptil N and Harisha S B, “Comparative Analysis of Multi-modal and Multi-focus Image Fusion Algorithms and Quality Assessment for Wireless Sensor Networks”, Proceedings of the 6th IETE National Conference on RF & Wireless,pp.197-202, 09-11 May 2013.
12. Ranjith Ptil N and Harisha S B, “Comparative Analysis of Multi-modal and Multi-focus Image Fusion Algorithms and Its Quality Assessment”, International Conference on Emerging Trends in Engineering (ICETE-13) ISBN: 978-93-82880 -29-5, NMAMIT, Nitte, pp 33-37, 15th to 16th may 2013.

Year: 2015

13. Harisha S B, Amarappa S and Dr. S V Sathyanarayana “ Automatic Speech Recognition – A Literature Survey on Indian Languages and Ground Work for Isolated Kannada Digit Recognition using MFCC and ANN”, International Journal of Electronics and Computer Science Engineering, ISSN 2277-1956,Volume 4 Number 1,February 2015, PP 91-105.
14. Harisha S B, Amarappa S and Dr. S V Sathyanarayana “Spoken Digit Recognition Based on Support Vector Machine for Kannada Language”, International Journal of Advanced Research in Computer Science and Software Engineering, ISSN: 2277 128X.Volume 5, Issue 11, November 2015, PP – 774-780.

	<p>Year: 2017</p> <p>15. Harisha S B, Amarappa S and Dr. S V Sathyanarayana “Kannada Speech Recognition Using MFCC and KNN Classifier for Banking Applications”, International Journal of Innovative Research in Computer and Communication Engineering, ISSN: 2320-9801. Volume 5, Issue 1, January 2017, PP – 65-75.</p>																																
9	Areas of Additional Technical knowledge acquired apart from the regular teaching subjects: Speech processing.																																
10	<p>Conferences attended details-year wise</p> <p>National Conferences:</p> <table border="1"> <thead> <tr> <th>Sl. No.</th> <th>Name of the conference</th> <th>Date</th> <th>Place</th> </tr> </thead> <tbody> <tr> <td>1.</td> <td>National conference on Signal processing and communication</td> <td>07th – 08th July 2006</td> <td>JNNCE, Shimoga</td> </tr> <tr> <td>2.</td> <td>5th National workshop n cryptography – NWC 2005</td> <td>12th – 14th Aug 2005</td> <td>JNNCE, Shimoga</td> </tr> <tr> <td>3.</td> <td>National conference on Vision, graphics and image processing</td> <td>02nd - 03rd March 2005</td> <td>JNNCE, Shimoga</td> </tr> </tbody> </table> <p>International Conferences:</p> <table border="1"> <thead> <tr> <th>Sl. No.</th> <th>Name of the conference</th> <th>Date</th> <th>Place</th> </tr> </thead> <tbody> <tr> <td>1.</td> <td>International Conference on Multimedia processing, Communication and Information Technology- MPCIT 2013</td> <td>19th – 21st Dec 2013</td> <td>JNNCE, Shimoga</td> </tr> <tr> <td>2.</td> <td>International joint colloquium on emerging technologies in computer electrical and mechanical – CEM 2011</td> <td>27th – 28th Dec 2011</td> <td>JNNCE, Shimoga</td> </tr> <tr> <td>3.</td> <td>Embedded Systems and VLSI Design</td> <td>04th – 08th Jan 2008</td> <td>HICC, Hyderabad</td> </tr> </tbody> </table>	Sl. No.	Name of the conference	Date	Place	1.	National conference on Signal processing and communication	07 th – 08 th July 2006	JNNCE, Shimoga	2.	5 th National workshop n cryptography – NWC 2005	12 th – 14 th Aug 2005	JNNCE, Shimoga	3.	National conference on Vision, graphics and image processing	02 nd - 03 rd March 2005	JNNCE, Shimoga	Sl. No.	Name of the conference	Date	Place	1.	International Conference on Multimedia processing, Communication and Information Technology- MPCIT 2013	19 th – 21 st Dec 2013	JNNCE, Shimoga	2.	International joint colloquium on emerging technologies in computer electrical and mechanical – CEM 2011	27 th – 28 th Dec 2011	JNNCE, Shimoga	3.	Embedded Systems and VLSI Design	04 th – 08 th Jan 2008	HICC, Hyderabad
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11	Books Published / IPRs/ Patents NIL																																
12	Professional Memberships: Member of IETE, Member of ISTE																																
13	Consultancy Activities																																
14	Grants fetched details																																

15	<p>Achievements and Recognitions / Awards</p> <p>(i) Participated in the National level e-Yantra Robotics Teacher Competition held on April 10, 2014 at IIT Bombay. Successfully demonstrated the minesweeper and secured THIRD PRIZE.</p> <p>(ii) Chief guest and session chair for the National Level Technical Conference e-Quest 15, organized by ECE Dept. KIT Tiptur on 29-04-2015</p>																																				
16	<p>Any other relevant information</p> <ul style="list-style-type: none"> • Admission Coordinator for the year 2016-17 and 2017-18. • GC Member of the JNNCE. <p>Short Term Courses Co -ordinated</p> <table border="1" data-bbox="277 703 1533 965"> <thead> <tr> <th>Sl.No</th> <th>Topic</th> <th>Organisation</th> <th>Period</th> </tr> </thead> <tbody> <tr> <td>1.</td> <td>VLSI System Design using Cadence Tool</td> <td>JNNCE, Shimoga</td> <td>25-01-2010 to 29-01 2010</td> </tr> <tr> <td>2.</td> <td>Linear Algebra and Its Applications</td> <td>JNNCE, Shimoga</td> <td>24-01-2011 to 28-01 2011</td> </tr> <tr> <td>3.</td> <td>Latex – A Tutorial Perspective (Hands on Training)</td> <td>JNNCE, Shimoga</td> <td>25-04-2014 to 26-04-2014</td> </tr> </tbody> </table> <p>Short Term Courses for which acted as resource person</p> <table border="1" data-bbox="277 1198 1533 1608"> <thead> <tr> <th>Sl.No</th> <th>Topic</th> <th>Participants & Organisation</th> <th>Period</th> </tr> </thead> <tbody> <tr> <td>1.</td> <td>Digital system design using VHDL</td> <td>JNNCE, Shimoga</td> <td>04-03-2005 to 12-03-2005</td> </tr> <tr> <td>2.</td> <td>Microcontroller and its Applications (PIC Microcontroller – 16F74)</td> <td>Government Residential Polytechnic for Women, Shimoga</td> <td>19-01-2007</td> </tr> <tr> <td>3.</td> <td>VLSI System Design using Cadence Tool</td> <td>JNNCE, Shimoga</td> <td>25-01-2010 to 29-01 2010</td> </tr> <tr> <td>4.</td> <td>Introduction to ARM Microcontroller</td> <td>DSDN Polytechnic</td> <td>12-04-2012</td> </tr> </tbody> </table>	Sl.No	Topic	Organisation	Period	1.	VLSI System Design using Cadence Tool	JNNCE, Shimoga	25-01-2010 to 29-01 2010	2.	Linear Algebra and Its Applications	JNNCE, Shimoga	24-01-2011 to 28-01 2011	3.	Latex – A Tutorial Perspective (Hands on Training)	JNNCE, Shimoga	25-04-2014 to 26-04-2014	Sl.No	Topic	Participants & Organisation	Period	1.	Digital system design using VHDL	JNNCE, Shimoga	04-03-2005 to 12-03-2005	2.	Microcontroller and its Applications (PIC Microcontroller – 16F74)	Government Residential Polytechnic for Women, Shimoga	19-01-2007	3.	VLSI System Design using Cadence Tool	JNNCE, Shimoga	25-01-2010 to 29-01 2010	4.	Introduction to ARM Microcontroller	DSDN Polytechnic	12-04-2012
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