



1	Name of the Teaching Faculty	Shwetha. H. R
2	Designation	Assistant professor
3	Email ID	shwethahr@jnnce.ac.in
4	Date of Joining the Institution	27/09/2013
5	Qualification	M.Tech, (Ph.D Registered under VTU)
6	Total Experience (Years)	Teaching 03 Industry 01 Research 0.5
7	Ph.D/Research Area	-----
8	Journals and Conference Papers Published details <ol style="list-style-type: none"> 1. Technical paper is published on “Ultra Low Power, area efficient Flip-flop Topology for Deep Sub-micron Region” in National Conference on “Emerging Trends in Engineering and Managements”, PES Institute of Technology and Management, Shivamogga-577204, March 1 & 2, 2013 2. Technical paper is published on. “Application of advanced High-k Metal Gates for high performance in transistor Switching speed” in National Conference on “Emerging Trends in Engineering and Managements”, PES Institute of Technology 	

	and Management, Shivamogga-577204, March 1 & 2, 2013
9	Areas of Additional Technical knowledge acquired apart from the regular teaching subjects VLSI Circuit Design (Cadence Tool)
10	Conferences attended details-year wise 1. International Conference on Multimedia processing, Communication and Information technology – MPCIT 2013 (19 th to 21 st Dec 2013) at JNNCE, Shivamogga
11	Books Published / IPRs/ Patents NIL
12	Professional Memberships IETE(AM 236757)
13	Consultancy Activities NIL
14	Grants fetched details NIL
15	Achievements and Recognitions / Awards NIL