

**JAWAHARLAL NEHRU NATIONAL COLLEGE OF ENGINEERING
SHIMOGA**

Faculty Bio-Data

1	Name of the Teaching Faculty	Ashwini S. R.		
2	Designation Contact Details: Mobile No: Email Id:	Assistant professor Dept.of TCE JNNCE, Shimoga 9886433003 srashwini@jnnce.ac.in		
3	Department	Telecommunication Engineering		
4	Date of Joining the Institution	07-09-2003		
5	Qualification with % of Marks & Grade with year	UG: BE, Electronics & Communication 72.5%, FCD, Kuvempu University 1998	PG: M.Tech, Networking & Internet Engineering, 82.33 %, I Rank with gold medal VTU, Belgaum 2009	Ph.D.: University of Mysore (Coursework completed)
6	Total Teaching Experience at JNNCE and at other places	IGNOU, Shimoga 2 Years JNNCE: 13 years 10 months	Industry Comat Technologies: 2 years	Research 2 years
7	PhD Guide? Give field & University No			
8	Journals and Conference Papers Published details National : Conference:2			



	<p>1. Ashwini.S.R & Shashi Kiran S, “Secured Communication using DES & Steganography Techniques”, National Conference on Signal Processing and Communication, NCSPC 2006, July 2006, JNNCE, Shimoga.</p> <p>2. Ashwini S R & Chethan K. R., “Blind DCT-2 robust video for MPEG-2 videos”. National conference on Image processing, Pattern recognition and computer vision, NCIPV August 2009, Mandya, Karnataka, India.</p>																																																											
9	<p>Areas of Additional Technical knowledge acquired apart from the regular teaching subjects: NIL</p>																																																											
10	<p>Conferences attended details-year wise: National:</p> <table border="1" data-bbox="277 734 1476 1034"> <thead> <tr> <th>Sl. No.</th> <th>Name of the conference</th> <th>Date</th> <th>Place</th> </tr> </thead> <tbody> <tr> <td>1.</td> <td>National conference on Kannadada sampoorna ganakikaranadatta bhashashastra, e-aadalita mattu mahiti tantragnanagala krodhikarana, KANCON 2006</td> <td>21st – 22nd Apr 2006</td> <td>JNNCE, Shimoga</td> </tr> <tr> <td>2.</td> <td>National conference on vision, graphics and image processing (NVGIP – 2005)</td> <td>2nd – 3rd Mar 2005</td> <td>JNNCE, Shimoga</td> </tr> </tbody> </table> <p>International:</p> <table border="1" data-bbox="277 1146 1476 1406"> <thead> <tr> <th>Sl. No.</th> <th>Name of the conference</th> <th>Date</th> <th>Place</th> </tr> </thead> <tbody> <tr> <td>1.</td> <td>International conference on multimedia processing, communication and Information technology</td> <td>19th – 21st Dec 2013</td> <td>JNNCE, Shimoga</td> </tr> <tr> <td>2.</td> <td>International joint colloquium on emerging technologies in computer electrical and mechanical – CEM 2011</td> <td>27th – 28th Dec 2011</td> <td>JNNCE, Shimoga</td> </tr> </tbody> </table> <p>workshops:</p> <table border="1" data-bbox="277 1482 1476 2072"> <thead> <tr> <th>Sl. No.</th> <th>Name of the workshop</th> <th>Duration</th> <th>Date</th> <th>Place</th> </tr> </thead> <tbody> <tr> <td>1.</td> <td>CMOS, Mixed Signal and Radio frequency VLSI Design</td> <td>2 weeks</td> <td>30th Jan – 4th Feb 2017</td> <td>JNNCE, Shimoga</td> </tr> <tr> <td>2.</td> <td>National Workshop on Cryptology, NWC-2016</td> <td>3 days</td> <td>11th -13th Aug 2016</td> <td>JNNCE, Shimoga</td> </tr> <tr> <td>3.</td> <td>A pedagogy for effective use of mathematics for research in science and technology</td> <td>1 week</td> <td>27th – 30th Jul 2015</td> <td>JNNCE, Shimoga</td> </tr> <tr> <td>4.</td> <td>Android application development and embedded android</td> <td>1 week</td> <td>14th – 23rd Feb 2015</td> <td>JNNCE, Shimoga</td> </tr> <tr> <td>5.</td> <td>Control Systems</td> <td>2 weeks</td> <td>2nd – 12th Dec 2014</td> <td>JNNCE, Shimoga</td> </tr> <tr> <td>6.</td> <td>Computer Networking</td> <td>2 weeks</td> <td>28th May – 5th Jul 2014</td> <td>JNNCE, Shimoga</td> </tr> </tbody> </table>	Sl. No.	Name of the conference	Date	Place	1.	National conference on Kannadada sampoorna ganakikaranadatta bhashashastra, e-aadalita mattu mahiti tantragnanagala krodhikarana, KANCON 2006	21 st – 22 nd Apr 2006	JNNCE, Shimoga	2.	National conference on vision, graphics and image processing (NVGIP – 2005)	2 nd – 3 rd Mar 2005	JNNCE, Shimoga	Sl. No.	Name of the conference	Date	Place	1.	International conference on multimedia processing, communication and Information technology	19 th – 21 st Dec 2013	JNNCE, Shimoga	2.	International joint colloquium on emerging technologies in computer electrical and mechanical – CEM 2011	27 th – 28 th Dec 2011	JNNCE, Shimoga	Sl. No.	Name of the workshop	Duration	Date	Place	1.	CMOS, Mixed Signal and Radio frequency VLSI Design	2 weeks	30 th Jan – 4 th Feb 2017	JNNCE, Shimoga	2.	National Workshop on Cryptology, NWC-2016	3 days	11 th -13 th Aug 2016	JNNCE, Shimoga	3.	A pedagogy for effective use of mathematics for research in science and technology	1 week	27 th – 30 th Jul 2015	JNNCE, Shimoga	4.	Android application development and embedded android	1 week	14 th – 23 rd Feb 2015	JNNCE, Shimoga	5.	Control Systems	2 weeks	2 nd – 12 th Dec 2014	JNNCE, Shimoga	6.	Computer Networking	2 weeks	28 th May – 5 th Jul 2014	JNNCE, Shimoga
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	7.	LaTex – A tutorial perspective	2 days	25 th – 26 th Apr 2014	JNNCE, Shimoga
	8.	Signals & systems	2 weeks	2 nd – 12 th Jan 2014	JNNCE, Shimoga
	9.	Database management system	2 weeks	21 st – 31 st May 2013	JNNCE, Shimoga
	10.	Research methodology	2 weeks	13 th – 24 th Jan 2013	JNNCE, Shimoga
	11.	Aakash for Education	2 days	10 th – 11 th Nov 2012	JNNCE, Shimoga
	12.	Secure communication and image processing	1 week	14 th – 18 th Mar 2012	JNNCE, Shimoga
	13.	Matlab and its applications	1 week	27 th – 31 st Jan 2012	JNNCE, Shimoga
	14.	Linear algebra and its applications	1 week	24 th – 28 th Jan 2011	JNNCE, Shimoga
	15.	VLSI system design using cadence tool	1 week	25 th – 29 th Jan 2010	JNNCE, Shimoga
	16.	Wireless sensor networks	2 days	9 th – 10 th May 2008	JNNCE, Shimoga
	17.	Digital signal processing	1 week	26 th – 30 th Dec 2006	JNNCE, Shimoga
	18.	Project identification, formulation and appraisal	1 week	4 th – 9 th Dec 2006	BIET, Davanagere
	19.	English communication skills	1 week	8 th – 12 th Aug 2006	JNNCE, Shimoga
	20.	DSP, VLSI and Embedded system design	3 weeks	19 th Jun – 09 th Jul 2006	SRM Institute of Science & Technology, kattankulathur, Tamil Nadu
	21.	Statistical modeling for technical education and researchers	2 weeks	9 th – 20 th Jan 2006	JNNCE, Shimoga
	22.	Constitution of India & Professional ethics	2 days	11 th – 12 th Aug 2005	RVCE, Bangalore
	23.	Digital system design using VHDL	1 week	4 th – 12 th Mar 2005	JNNCE, Shimoga
	24.	Embedded controllers	2 weeks	12 th – 24 th Jul 2004	JNNCE, Shimoga
	25.	Recent advances in image processing	1 week	1 st – 5 th Mar 2004	JNNCE, Shimoga
11	Books Published / IPRs/ Patents NIL				
12	Professional Memberships: Member of IETE, ISTE				

13	Consultancy Activities NIL
14	Grants fetched details NIL
15	Achievements and Recognitions / Awards NIL
16	Any other relevant information NIL