

**Registration form**

**Five Days  
Faculty Development Program**

**on  
“Arm Microcontroller & Embedded System  
Using Arm Cortex M”**

**11<sup>th</sup> - 15<sup>th</sup> December 2017**

**Name:**

**Designation:**

**Department:**

**Email-ID:**

**College:**

**Mobile No.:**

**DD No.:**

**Date:**

**Bank:**

**Accommodation: Yes/No**

**Date:**

**Place:**

**Signature of the Applicant**

**Signature of the Head of Department  
(with seal)**

**Organizing committee**

**Dr.H R Mahadevaswamy  
Principal**

**Dr. Manjunatha P  
HOD, ECE Department**

**Co-ordinators**

**Mr. Sharath S M  
Asst Prof, ECE Department**

**Mrs. Prema K N  
Asst Prof, ECE Department**

**Mrs. Shwetha H R  
Asst Prof, ECE Department**

**Five Days  
Faculty Development Program  
on  
“Arm Microcontroller & Embedded System  
Using Arm Cortex M”**

**11-12-2017 to 15-12-2017**



**Dept. of Electronics & Communication Engg.  
Jawaharlal Nehru National College of  
Engineering  
Shivamogga 577204, Karnataka**

**In Association with**

**The Institution of Electronics and Telecommunica-  
tion Engineers, Shivamogga Center**

**About the College:**

JNNCE was established in the year 1980 by the National Education Society. The college is affiliated to Visvesvaraya Technological University (VTU), Belgaum and recognized by AICTE, New Delhi. It offers 7 UG and 7 M.Tech Program besides MBA and MCA. It has 7 Research centers recognized by VTU. Spread over 52 acres, the campus has all the amenities to cater to the needs of about 3500 students and 400 faculty and staff. The college is regarded as one of the excellent centers for technical education and research. J. N. N. C. E is ranked 47<sup>th</sup> among top 75 engineering colleges in India and 8<sup>th</sup> in Karnataka as per OUT Look –MDRA ranking survey 2017 consecutively .

**About the Department:**

The Department of Electronics & Communication Engineering was started in the year 1980.

**About the Programme:**

This Faculty Development Program “Arm Microcontroller & Embedded System Using Arm Cortex M” is being organized for five days to provide hands on ARM cortex M3 microcontroller platform for faculties.

**Topics to be Covered: (As per VTU Syllabus)**

- Basics on ARM-32 bit Microcontroller.
- ARM Cortex M3 Instruction sets and Programming.
- Embedded System Concepts and Components
- RTOS and IDE for Embedded system Design.
- Hands on workshop with ESA KEIL MCB1760 board which includes usage of 6. ARM KIT, Installation of Keil software, downloading & execution of software and interfacing programs.

**Participation:**

Faculty members from ECE / TCE and EEE departments of Engineering colleges can attend the workshop.

**Registration details:**

The registration form should reach the convener, along with the registration fee of Rs. 1,000/-.

Accommodation will be provided in college hostel/guest house.

The registration fee should be paid through DD drawn in the favor of “ISF, JNNCE”, Payable at “Shimoga”.

**For online registration:**

Account No.: 135210002734  
IFSC:KKBK0008292

**Address for Communication:**

Mrs. Shwetha HR  
Asst. Prof.  
Dept. of ECE,  
JNN College of Engineering,  
Cell: 7204443953  
Email:[shwethahr@jnnce.ac.in](mailto:shwethahr@jnnce.ac.in)

Mrs. Prema K.N.  
Asst. Prof.  
Dept. of ECE  
JNN College of Engineering  
Cell : 8277324422  
Email: [premakn@jnnce.ac.in](mailto:premakn@jnnce.ac.in)

**Important Dates:**

Last Date for registration: 8th Dec 2017.

Workshop Dates: 11th to 15th December 2017

Venue: AD block Seminar Hall ,Silver Jublee Building, JNNCE

Timing: 9.00 am to 5.00 pm

## Workshop Report

The Department of Electronics and Communication Engineering in association with IETE Shivamogga Center and ISTE organized a Five day workshop on "ARM Microcontroller & Embedded System Using ARM Cortex M3" for the faculty of Electronics and Communication Engineering and Telecommunication Engineering. The FDP has been mainly designed to fit the syllabus of ARM Lab of 6<sup>th</sup> Semester ECE/TCE. Mr Sharath S. M, Mrs Prema K. N and Mrs Shwetha H. R took the responsibility of coordinating the workshop under the careful guidance of Dr. Manjunatha P, HOD E&C Department.

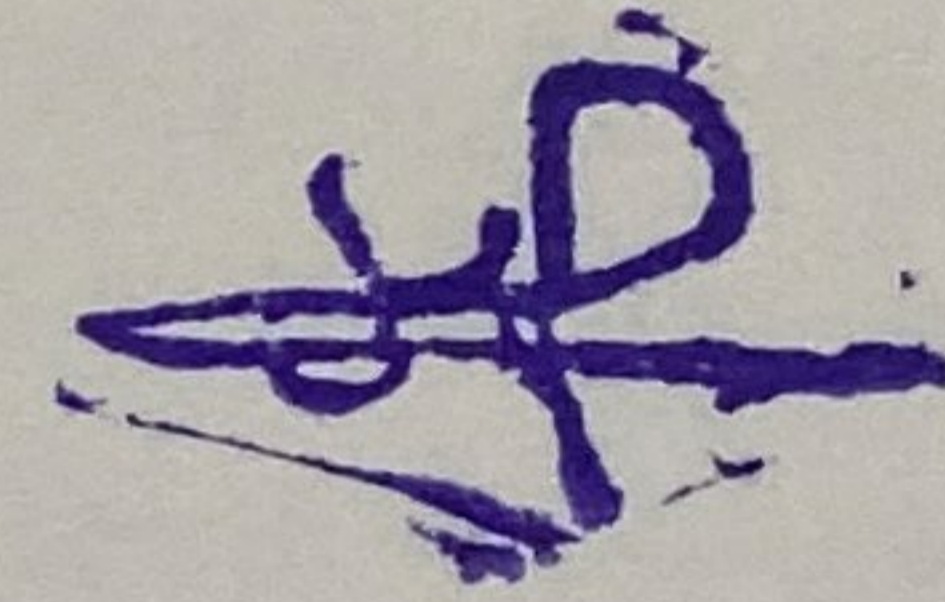
The workshop was inaugurated by Sri S. N. Nagaraja, Secretary NES Shivamogga, Mr Jeevan B. Katariya Director, SPJ, Embedded Technologies Pvt Ltd and Dr. H.R Mahadevaswamy, Principal J.N.N.C.E Shivamogga. Dr. Manjunatha P, HOD E&C Department highlighted the importance and relevance of the workshop for the faculty and students. Dr R Srinivasa Rao Kunte Professor E&C Department and BOS Chairman also strengthened the importance of the workshop for the faculty. The inaugural function was immediately followed by the theory sessions on ARM microcontrollers.

The Day 1 after the inaugural function was headed by the resource person Mr Jeevan Katariya. He gave an overall picture of the ARM Microcontroller architecture by comparing with the previous microcontroller architectures. The session was very interactive and the post lunch session began with the awareness for the importance of practical experiments for engineering students by Mr Sunil Shabhatnavar SPJ, Embedded Technologies Pvt Ltd. Later the participants were guided to the Lab sessions and the "how to" of ARM Lab was explained in detail by Mr Jeevan.

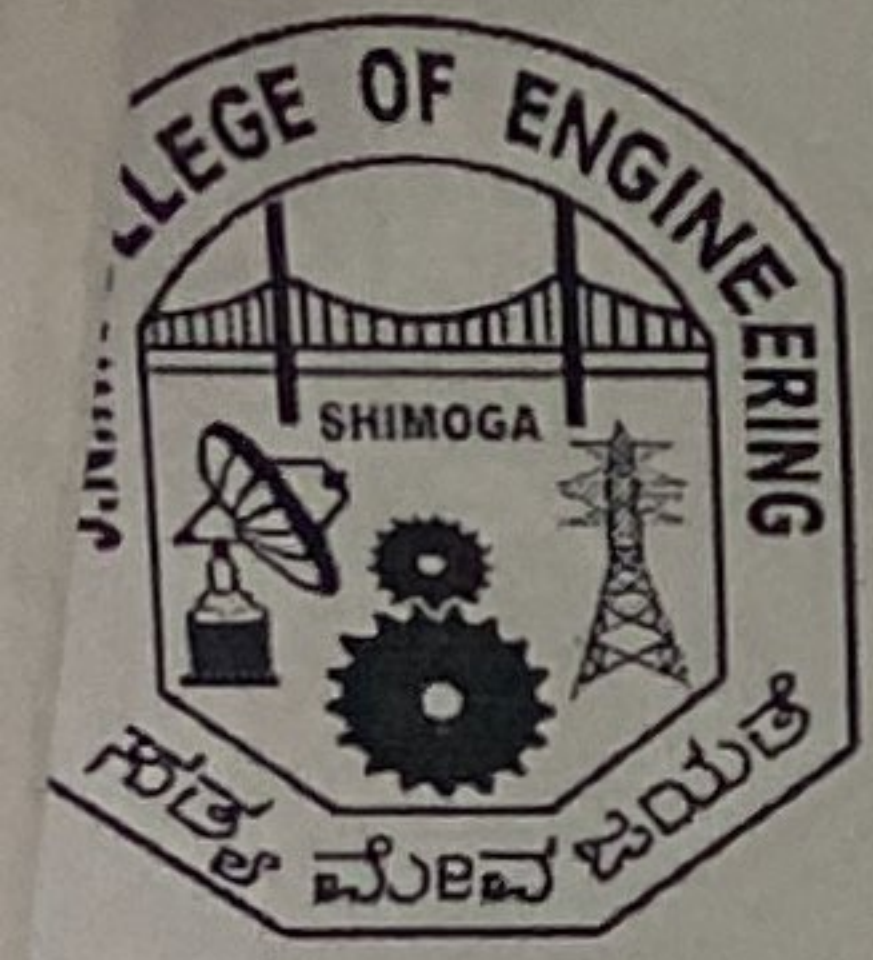
The Day 2 session started with Hands on Session on ARM microcontroller programs. The programs included blinking an LED, LCD, Seven segment display and UART. Mr Jeevan and his team member Mr Jeevan gave an depth knowledge of programming and allowed the participants to explore the power of the ARM microcontroller architecture. Participants got equipped with the knowledge of I/O ports, registers syntax of the code during the second day.

The Day 3 session was mainly dedicated to the lab programs pertaining to the syllabus. Mr Nandesh and Mr Kiran were the resource persons for the Day 3 session. It got kicked off by the good old stepper motor interfacing, Keypad, etc. The experts also shared several example programs which are very helpful in conducting Lab Experiments. The later part of the session was dedicated to RTOS applications using ARM controller, which is the distinguishing property of the state of the art microcontroller,

HA  
The workshop also aimed at the 2 day sessions on NS2 simulator as a sequel to the ARM microcontroller. The fourth and fifth day was completely dedicated to the hands-on on NS2. The Day 4 began with introduction to Network simulators and a brief overview of NS2 simulators. Dr Kiran M, Assistant Professor of ISE with his team comprising Mr Vishwas.C.G.M, Assistant Professor of ISE and Soumya G V, Assistant Professor of ISE led the handson session. They educated the participants beginning from installation of NS2 in Linux platform to the usage of NS2 simulator for basic topologies. The session got completed in quiz . The day 5 was completely dedicated to the lab programs of CCN Lab prescribed by the VTU for 6<sup>th</sup> Semseter ECE.



Head of the Department  
Electronics and Communication  
J.N.N. College of Engineering  
SHIMOGA-577 204.



सह वीर्यं करवा  
Institution of Electronics and Telecommunication Engineers

Department of Electronics and Communication Engineering  
in Association with IETE and ISTE

Attendance

Date: 11.12.2017

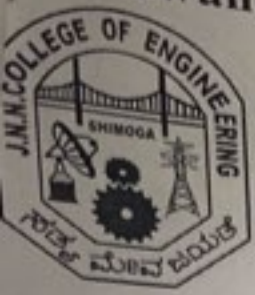
JNNCE Faculty

Day 1

Sl.No	Name of the Participant	Dept	Session 1	Session 2
1	Dr R Srinivas Rao Kunte	ECE	<i>[Signature]</i>	<i>[Signature]</i>
2	Dr S V Sathyanarayana	ECE	<i>[Signature]</i>	
3	Shwetha B	ECE	<i>[Signature]</i>	<i>[Signature]</i>
4	Darshan K V	ECE	K.V. Darshan	Ab
5	Smitha S M	ECE	<i>[Signature]</i>	<i>[Signature]</i>
6	Pradeep H K	ISE	<i>[Signature]</i>	<i>[Signature]</i>
7	Sathyanarayana K B	ISE	<i>[Signature]</i>	<i>[Signature]</i>
8	Benak Patel M P	TCE	<i>[Signature]</i>	<i>[Signature]</i>
9	Harish T S	TCE	<i>[Signature]</i>	<i>[Signature]</i>
10	Prashanth G S	ECE	<i>[Signature]</i>	<i>[Signature]</i>
11	Bhuvana Khare	TCE	<i>[Signature]</i>	<i>[Signature]</i>
12	Nalina S B	ECE	<i>[Signature]</i>	<i>[Signature]</i>
13	Roopa B S	ECE	<i>[Signature]</i>	<i>[Signature]</i>
14	Radhika S N	CSE	<i>[Signature]</i>	<i>[Signature]</i>
15	Chaitra K V	CSE	<i>[Signature]</i>	<i>[Signature]</i>
16	Pradeep S C	ECE	-	Ab
17	Abhijith N	ECE	<i>[Signature]</i>	Ab
18	Ajay Betur P	ECE	Ab	
19	Madhukar C S	TCE	Ab	Ab
20	Anil Kumar J	ECE	<i>[Signature]</i>	Ab
21	Harisha S B	TCE	<i>[Signature]</i>	Ab
22	Aparna	TCE	<i>[Signature]</i>	<i>[Signature]</i>
23	G C Kamalamma	ECE	<i>[Signature]</i>	<i>[Signature]</i>
24	Sunil M. D.	ECE	<i>[Signature]</i>	Ab

25. Sharath. S.M.  
26. Prema. K.M  
27. Shwetha - H.R

ECE *[Signature]*  
ECE *[Signature]*  
ECE *[Signature]*  
*[Signature]*  
*[Signature]*



Department of Electronics and Communication Engineering  
in Association with IETE and ISTE

Attendance  
Day2

Date:12.12.2017

JNNCE Faculty

Sl.No	Name of the Participant	Dept	Session1	Session2
1	Dr R Srinivas Rao Kunte	ECE	Ab	Ab
2	Dr S V Sathyanarayana	ECE	<del>S.V.S</del>	<del>S.V.S</del>
3	Shwetha B	ECE	<del>Shwetha B</del>	<del>Shwetha B</del>
4	Darshan K V	ECE		
5	Smitha S M	ECE	SM	SM
6	Pradeep H K	ISE		
7	Sathyanarayana K B	ISE	S.K.B	S.K.B
8	Benak Patel M P	TCE	M.P.	M.P.
9	Harish T S	TCE	H.T.S	H.T.S
10	Prashanth G S	ECE	P.G.S	P.G.S
11	Bhuvana Khare	TCE	B.Kh	B.Kh
12	Nalina S B	ECE	Nalina	Nalina
13	Roopa B S	ECE	R.B.S	R.B.S
14	Radhika S N	CSE	R.S.N	R.S.N
15	Chaitora K V	CSE	C.K.V	C.K.V
16	Pradeep S C	ECE	P.S.C	P.S.C
17	Abhijith N	ECE	Ab	Ab
18	Ajay Betur P	ECE	<del>A.B.P</del>	<del>A.B.P</del>
19	Madhukar C S	TCE	M.C.S	M.C.S
20	Anil Kumar J	ECE	Ab	Ab
21	Harisha S B	TCE	H.S.B	H.S.B
22	Aparna	TCE	Aparna	Aparna
23	G C Kamalamma	ECE	G.C.K	G.C.K
24	Sunil M D	ECE	S.M.D	S.M.D



Department of Electronics and Communication Engineering  
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25	Prema K N	ECE	<i>[Signature]</i>	<i>[Signature]</i>
26	Shwetha H R	ECE	<i>[Signature]</i>	<i>[Signature]</i>
27	Sharath S M	ECE	<i>[Signature]</i>	<i>[Signature]</i>

M.Tech Students, DECS

Day2

Date: 12.12.2017

Sl.No	Name of the Participant	College	Session1	Session2
1.	Ayaz Ahmed	JNNCE	<i>Ayaz Ahmed</i>	<i>Ayaz Ahmed</i>
2.	Shiraaz M N		<i>Shiraaz</i>	<i>Shiraaz</i>
3.	Sobiya Suha M		<i>Sobiyasuha</i>	<i>Sobiyasuha</i>
4.	Sandya N V		<i>Sandya</i>	<i>Sandya</i>
5.	Supriya Patil S		<i>Supriya Patil</i>	<i>Supriya Patil</i>
6.	H R Artpitha		<i>H Artpitha</i>	<i>H Artpitha</i>
7.				