## Jawaharlal Nehru National College of Engineering, Shimoga.

## Department of Electrical and Electronics Engg.

Internal Quality Assurance Cell

Date: 09-04-2018

One day workshop on "Basic Electronics Engg. - a practical approach",

## Submitted:

Sub: Report on One day Workshop conducted -reg.

A one day workshop on "Basic Electronics Engg. – a practical approach", for the benefit of 1<sup>st</sup> year EEE branch students, was organized on Saturday, the 7<sup>th</sup> April 2018, from 10-30AM to 5-30PM. Student Volunteers from 4<sup>th</sup> sem. EEE took the initiative to explain and demonstrate various types of components, equipments and models. Earlier, at 11:00 AM, the programme was formally inaugurated by Dr. H.R. Mahadevaswamy, Principal, JNNCE accompanied by Dr. L.K.Sreepathi, Vice-Principal and Dr. S. Surendra, H.o.D., EEE dept. Prof.N.G.Ajjanna, Staff Coordinator, POWERTRON 2018-19, along with the workshop Coordinators Prof. Vidyashankar M. and Mr. Chandrashekhara S., Foreman, EEE dept., which was conducted at the Dept. Seminar Hall.

Later Dr.H.R.Mahadevaswamy, Principal, J.N.N.C.E., addressed the gathering and asked to get the highest benefits from these kinds of Workshops, where the senior students have taken initiative to multiple understand the concepts much clear.

At first a power point presentation, was run on Basic Electronics. The 42 members' student par (the list is enclosed herewith) were divided into TWO batches and were moved to two Laboratories, where pl showed various electronics components such as Diodes, Resistors, Capacitors—their types, their coding, pin identifications and demonstrated different types of equipments such as a D.C.R.P.S., A.F.Os., C.R.Os., Digital Multimeters, Solder less Bread boards, how to rig up simple circuits, how to test them and so on. A few experimental set ups on Diode rectifiers, Op-amp applications, Basic logic gates, Flip-flops and simple circuits were also demonstrated.

The 22 members' student Volunteers under the group named "TEAM JAGRUTI", from 4<sup>th</sup> sem. EEE., (the list is enclosed herewith) involved themselves, for explanation and demonstration. They were given training by us for three days.

Overall it was well received by the participants, which was expressed in the feedback. The participants thanked their seniors and the dept. for organizing such a workshop which was very much useful.

Finally, it was concluded at 6-00PM. A Bus trip was arranged from JNNCE to Shimoga Bus stand.

Encl: a. List of the Participants, b. List of the Volunteers, c. Feedback forms d. Photo session

(Prof. Vidyashankar M.) (Mr.Chandrashekhara S.)

**Workshop Coordinators** 

(Dr. S.Surendra)

Prof. & Head, EEE dept.

Professor and Head

Dept. of Electrical & Electronics Engg.

Jawaharlal Nehru

National College of Engineering SHIMOGA-577 204. (Dr. H.R. Mahadevaswamy)

Principal, JNNCE

Principal

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