

Jawaharlal Nehru National College of Engineering, Shimoga
Department of Electronics and communication Engineering
Internal Quality Assurance Cell

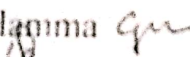





Date: 02/02/2018

All the 4th semester students are hereby informed to attend the "Workshop on Fundamentals of Linear Integrated Circuits and Analog Communication" on 5th and 6th February 2018 in AEC and DEC labs without fail.

Workshop Schedule:

Date	Time	Batch
05/02/2018	2.00pm-5.00pm	A1,A2, A3,A4
06/02/2018	2.00pm-5.00pm	B1,B2, B3,B4

Coordinators:

G C Kamalamma 
Sunil M D 
Roopa B S 
Smitha S M 
Prema K N 
Sumathi K 


HOD

Dr. P Marjumaratha

Lab Instructors:

Manjula B
Badrish K P

Jawaharlal Nehru National College of Engineering, Shimoga
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“Workshop on Fundamentals of Linear Integrated Circuits and Analog Communication”

Student Attendance

Class : 4th A Sec

Date:05/02/2018

SL. NO.	USN	Name	Signature	
1	4JN14EC030	HALLEPPA		
2	4JN16EC001	ABHIRAMA B A		
3	4JN16EC002	ADITHYA K		
4	4JN16EC003	AFSANA S K		
5	4JN16EC004	AKSHAY ANIL NAIK		
6	4JN16EC005	AMANI G		
7	4JN16EC006	ANANTRAYA PRABHAKAR BALGI		
8	4JN16EC007	ANITHA K P		
9	4JN16EC008	ASFIYA BANU		
10	4JN16EC009	ASHA H N		
11	4JN16EC011	ATHREYA S R		
12	4JN16EC012	BHOOMIKA K V		
13	4JN16EC013	CHANDAN HEGDE		
14	4JN16EC015	CHENNA KALYANA KEERTHI VARDHAN		
15	4JN16EC016	CHINMAYA M N		
16	4JN16EC017	CHITRA K B		
17	4JN16EC018	DARSHAN PATIL D		
18	4JN16EC019	DEEKSHA A		
19	4JN16EC020	DEEKSHA B GOUDAR		
20	4JN16EC021	DISHA P NADIG		
21	4JN16EC022	GAGAN G L		
22	4JN16EC023	GANESH GOPAL SHET		
23	4JN16EC024	GAYATHRI M		
24	4JN16EC025	GOUSEPASHA		
25	4JN16EC026	GOUTHAM S TANTRI		
26	4JN16EC027	GURURAJ A M		
27	4JN16EC028	HAMSINI SUDHEENDRA		
28	4JN16EC029	HANUMANTHAGOWDA VASUDEVA PATIL		
29	4JN16EC030	HARSHAD G NAIK		
30	4JN16EC031	HARSHITA ANAND SHETTY		
31	4JN16EC032	HEMANTH JADHAV C		

32	4JN16EC033	HRUTHIK K B	HRUTHIK	HRUTHIK
33	4JN16EC034	KARAN R	KARAN R	KARAN R
34	4JN16EC036	KARTHIKA J	KARTHIKA	KARTHIKA
35	4JN16EC037	KIRAN L		
36	4JN16EC038	KUSUMA S	KUSUMA S	KUSUMA S
37	4JN16EC039	MADHU A P		
38	4JN16EC040	MAHANTESH M		
39	4JN16EC042	MANASA R	Manasa R.	Manasa R.
40	4JN16EC043	MANJU KARABASAPPA UKKUND	MANJU	MANJU
41	4JN16EC044	MANJUNATHA M J	Manjunath.mj	Manjunath.mj
42	4JN16EC045	MANJUNATHA VIJAYAPPA BANGERA		
43	4JN16EC046	MEENAKSHI N	Meenakshi N.	Meenakshi N.
44	4JN16EC047	MEGHANA G S	Meghana G S	Meghana G S
45	4JN16EC048	MEGHANA Y M	Megha.Ym	Megha.Ym
46	4JN16EC049	MISBA AFREEN	Misba Afreen	Misba Afreen
47	4JN16EC050	MOHAMMED AYUB KHAN		
48	4JN16EC051	MOHSINA AL VRA	Mohsina	Mohsina
49	4JN16EC052	MURALI S	Murali S	Murali S
50	4JN16EC053	MURUGENDRA KUMAR B		
51	4JN16EC054	NAMRATHA L R	Namratha.L.R	Namratha.L.R
52	4JN16EC055	NAYANA R J	Nayana R.J	Nayana R.J
53	4JN16EC419	NAYANA H G	Nayana H.G	Nayana H.G
54	4JN17EC400	AKHIL SANTHOSH	Akhil	Akhil
55	4JN17EC401	ARCHANA V	Archana.V	Archana V
56	4JN17EC402	B POOJA	Pooja	Pooja
57	4JN17EC405	DARSHAN R	Darshan R	Darshan R
58	4JN17EC407	JAMUNA V	Jamuna V	Jamuna V
59	4JN17EC411	NEHA H S	Neha	Neha
60	4JN17EC418	SINDHU N	Sindhu	Sindhu
61	4JN17EC415	SEVANTHI B A	Sevanti	Sevanti
62	4JN17EC417	SHRAVANI	Shravan	Shravan
63	4JN17EC419	SPOORTHI S T		
64	4JN17EC420	VARSHINI L	Varshini.L	Varshini.L
65	4JN17EC422	VIJAY KUMAR NAYKA	Vijay	Vijay
66	4JN16EC119	VEENA K V	Veena	Veena
67	4JN15EC004	AISHWARYA S		

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Workshop report

Date:06/02/2018

The department of Electronics & Communication Engineering organized two days workshop on **Fundamentals of Linear Integrated Circuits and Analog Communication: Timer and op-amp** and their applications respectively.

The main aim of the workshop is to improve the practical knowledge in the students so that it will help them in doing hobby projects and simple projects by their own.

The FDP has conducted for 4th semester Electronics students. Co-ordinated by G C Kamalamma, Roopa B S, Smitha S M, Prema K N, Sumathi K under the guidance of Dr Manjunatha P, HOD, ECE Department.

The day one session started with Presentation on introduction about timer and op-amp by Dr. Manjunatha P, followed hands on session in AEC laboratory for A section students.

The day two session started with Presentation on introduction about timer and op-amp by Dr. Manjunatha P, followed hands on session in AEC laboratory for B section students.

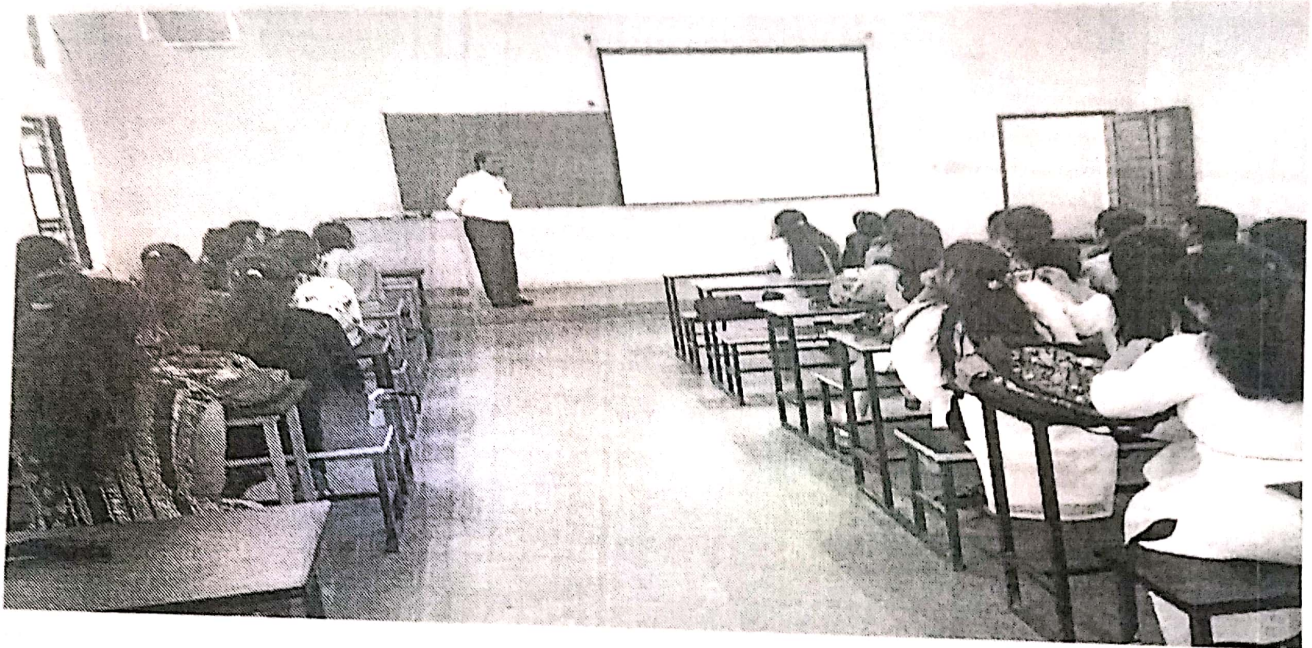


Dr. Manjunatha P

HOD

Head of the Department
Electronics and Communicat
J.N.N. College of Engineerin
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Photos of the Workshop