National Education Society ® JAWAHARLAL NEHRU NATIONAL COLLEGE OF ENGINEERING Shivamogga - 577205 Department of Electronics and Communication Engineering Two days Workshop on Electronic Devices '30.7.2019 - 31.7.2019'

1. Presentation : By 3rd Year Students

Topics covered:

Introduction to CRO, Signal Generator, Different Power Supplies, Resistors, Capacitors, Inductors, LED, LCD Diodes, Transistors, FETs and Applications Transformer

- 2. Lab sessions: Conducted in Analog Electronics , Digital Electronics and Advanced Communication Labs
- 3. Quiz session
- 4. Feedback from participants

Co-Ordinators:

Dr.Manjunatha P Roopa B S Prema K N Smitha S M

Student Valentiers : Manoj M, Anoop K S , Mohan Kumar K P, Subramanya C, Chandan P, Prajwal A C, Suhas S, Prajwal P, Sujeeth U S, Sairaj S V, Sanjay D Gowda

Timings: 2.30PM to 5.30PM on both the days

Presentation Venue: EC106

Note: 31.7.2019 'A' Section

31.7.2019 'B' Section

HOD,ECE

Dr.P Manjunatha

Head of the Department Electronics and Communication J.N.N. College of Engineering SHIMOGA-577 204.

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JAWAHARLAL NEHRU NATIONAL COLLEGE OF ENGINEERING

<u>Shivamogga - 577205</u> Department of Electronics and Communication Engineering Two days Workshop on Electronic Devices ' 30.7.2019 - 31.7.2019 '

A Report

Department of Electronics and communication engineering organized two days workshop on Electronic Devices from 30.07.2019 to 31.07.2019. The workshop started by Dr.P.Manjunatha HOD and professor, by giving brief about Electronic devices, then students explained basics of active, passive components and its applications and also gave brief description about CRO, signal generators, transformers and different power supplies

The presentation session is followed by lab sessions in analog electronics, digital electronics

and advanced communication labs, students explained the operation of signal CRO and different power supplies, identification of values of resistors, capacitor of probe in CRO, checking of transistor, diode, Zener diode and FET using also to identify the terminals of BJT and FET.

In digital electronics lab, students explained the trainer-kit, how to implement the logic gates and Boolean expressions.

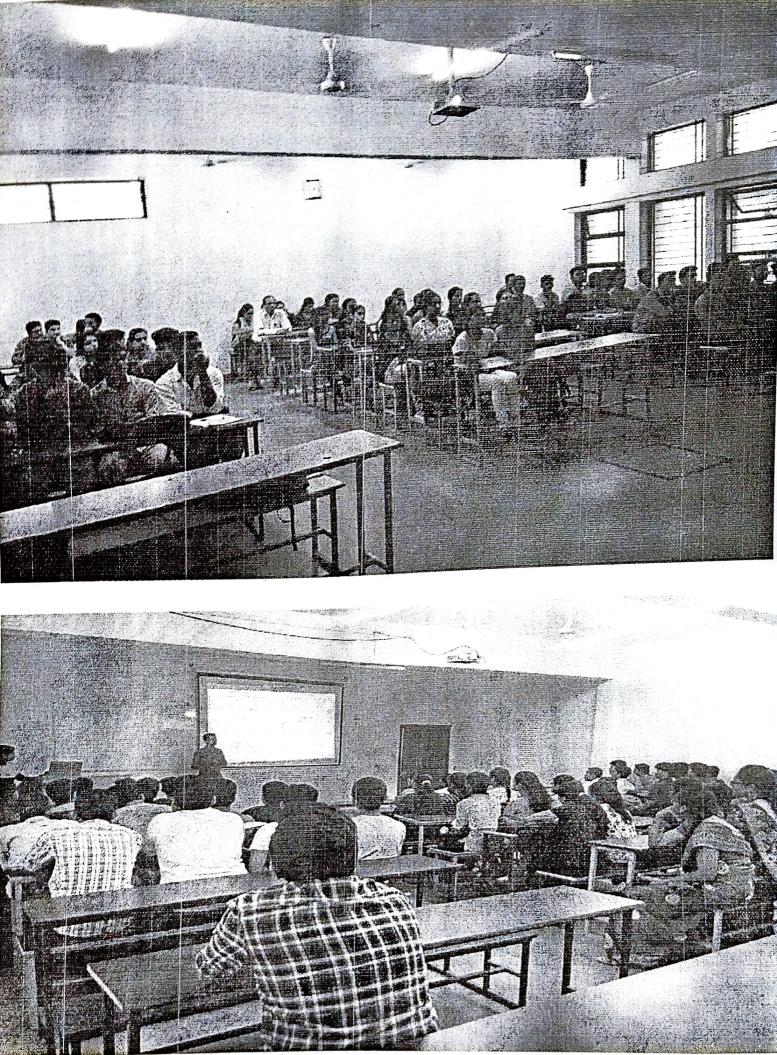
After the lab session quiz was given to the students, then feedback from students was taken to improve the next coming workshops. Then the workshop was concluded by Dr. P Manjunatha, HOD.

HOD, ECE

Dr.P Manjunatha Head of the Department Electronics and Communication J.N.N. College of Engineering

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<u>National Education Society ®</u> JAWAHARLAL NEHRU NATIONAL COLLEGE OF ENGINEERING

Shivamogga - 577205

Department of Electronics and Communication Engineering Two days Workshop on Electronic Devices Date: 31/7/2019 3rd ECE 'B' Section

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