

REGISTRATION FORM

Five Days National Level Workshop On "DSP and Image Processing through Machine Learning"

08th - 12th January 2019

1. Name. _____
2. Designation. _____
3. Department. _____
4. Postal Address. _____
5. Phone No. _____
6. Email-ID. _____

Registration Fee Details :

Transaction No :

Date :

Signature of the Applicant

Mr./Ms./Dr. _____ is an employee of
our Institute and is hereby permitted for the above
Workshop.

Date :

Head of the Institution

Place :

CHIEF PATRONS

Sri A S Venkatesanatha, President NES
Sri T R Ashokumararajyena Setty, Vice President NES
Sri S N Nagaraj, Secretary NES
Sri Anandappa Kibatti, Joint Secretary NES
Sri C R Nagaraj, Treasurer, NES

PATRONS

Dr. H R Mahadevaswamy, Principal JNNCE, Shimoga
Dr. L K Sreepathi, Vice Principal JNNCE, Shimoga

CONVENER

Dr. Manjunatha P, HOD, Dept. of ECE, JNNCE

CO-CONVENERS

Mrs. Roopa B.S, Asst. Professor Dept. of ECE, JNNCE
Mrs. Prema K.N, Asst. Professor Dept. of ECE, JNNCE
Mrs. Smitha S.M., Asst. Professor Dept. of ECE, JNNCE

ORGANIZING COMMITTEE

All Teaching and non Teaching staff of Electronics
& Communication Engineering Department

ADVISORY COMMITTEE

Dr. Ashok Rao
Former Head, Network Project, CEDIT, IISc, Bangalore
Dr. T N Nagabhushan
Principal and Professor, SJCE, Mysore
Dr. B G Sangameshawara
Vice Chancellor, JSS Science and
Technological University, Mysore
Dr. K N Subramanya
Principal and Professor, RVCE, Bangalore

ADDRESS FOR COMMUNICATION

Mrs. Roopa B.S Asst. Professor
Ph : 9480447444
Mrs. Prema K.N. Asst. Professor
Ph : 8277324422
E-mail : roopabs@jnncce.ac.in ; premakn@jnncce.ac.in



NES Education Society (R)
Jawaharlal Nehru National College of
Engineering, Shimoga - 577 204



[Affiliated to VTU Belagavi, Recognized by Govt. of Karnataka,
Accredited by NAAC and Approved by AICTE, New Delhi]

Five Days National Level Workshop
On

"DSP and Image Processing
through Machine Learning"

In Association with ISTE & IETE



From : 8th to 12th January- 2019

Organized by
Department of Electronics &
Communication Engineering



ABOUT THE COLLEGE

JNCE was established in the year 1980 by the National Education Society. The college is affiliated to Visvesvaraya Technological University (VTU), Belgaum and recognized by AICTE, New Delhi. Institution is NAAC accredited and UGC 2f and 12B certified. It offers 7 UG and 7 M.Tech program besides MBA and MCA. It has 7 Research Centers recognized by VTU. Spread over 52 acres, the campus has all the amenities to cater to the needs of about 3500 students and 400 faculty and staff. All the departments have good infrastructural facilities with highly qualified and experienced faculty, staff and well equipped laboratories. The college is regarded as one of the excellent centers for technical education and research. JNCE is ranked 42nd among top Engineering colleges in India and 7th in Karnataka as per Outlook survey 2018. The college is providing its fruitful services in the 38th year. The college organizes international and national workshops, conferences, seminars and short term courses on a regular basis. The institution has IEEE as well as ISTE members.

ABOUT THE DEPARTMENT

The Department of Electronics & Communication Engineering was started in the year 1980. Department have well equipped laboratories, qualified and experienced staff to cater to the needs of students of UG program and PG program M.Tech in Digital Electronics and Communication Systems. The ECE department has been recognized as research center under which 16 research scholars are pursuing Ph.D under VTU Belgaum. UG and PG students are actively involved in carrying out their projects at New Age Incubation Network. The department has IETE and ISF forums. The Institute of Electronics and Telecommunication Engineers (IETE) center is the national apex body of professional engineers devoted to the advancements of Electronics, Telecommunications, IT and allied areas. The 56th center of IETE was inaugurated at Jawaharlal Nehru national College of Engineering Shivamogga on 1st March 2009. IETE serves more than 69,000 members both individual and industrial organizations through its 65 centers spread all over India including one abroad.

DEPARTMENT VISION

To be a premier centre of education and research in Electronics & Communication Engineering for producing competent engineers to cater to the needs of industry and society.

DEPARTMENT MISSION

- * Impart quality education through innovative and continuous teaching - learning process
- * Groom the students with required foundations of Electronics & Communication Engineering and inculcate in them the professional ethics & human values.
- * Train the students and the faculty in state - of - the-art technologies and motivate them for higher studies & innovation based research for the needs of the industry and society.
- * Create centers of excellence in the field of Electronics & Communication Engineering in collaboration with the industries and academic organizations.

ABOUT THE WORKSHOP

This workshop is being organized for Five days to provide a platform for faculties, researchers and technologists, to understand the concepts of Pattern Processing and machine learning to solve complex engineering problems. With this in view the course would devote time talks on the topics cited below along with 'Hands-on Sessions'.

TOPICS

- * Introduction to Pattern Processing, Machine Learning, Mathematics of Machine Learning
- * Advanced Algorithms for Machine Learning
- * Biomedical Applications using Machine Learning
- * CNN, Deep Learning techniques and Applications
- * Laboratory sessions using Python, R & MATLAB

PARTICIPATION

The Interested academicians from Electronics, Telecommunication, Computer science, Information science, Industry personnel, researchers and allied disciplines are invited to attend the five days Workshop.

RESOURCE PERSONS

Resource persons from distinguished Research Institutes like IISc, NITK, CMI, industries and colleges will deliver expert lectures and practical sessions

REGISTRATION DETAILS

A registration fee of Rs. 1000 will be charged for each participant. The registration fee should be paid through online, the details of which are given below.

Bank	:	CANARA BANK
Branch	:	JNCE Campus, Shivmoga
SB A/c. No.	:	6322101000028
IFSC code	:	CNRB0006322

Scanned copy of the application with duly forwarded by the HOD and Principal, must be sent along with online transaction to the conveners email id. The number of participants is limited to 45 on first come first serve basis.

Note : Registration fee does not include accommodation. However accommodation may be provided to the participant with a nominal fee in the college hostel. No TA/DA will be provided for the participants.

IMPORTANT DATES

Submission of application forms by :
04th January 2019.
Workshop Dates : 8th to 12th January -2019.

VENUE :

Seminar hall (Ad-Block), Computer Lab (ECE Dept.)
Timings: 9.00 am to 5.30 pm



**National Education Society(R)
Jawaharlal Nehru National College of
Engineering, Shimoga**



**Internal Quality Assurance Cell(IQAC)
Department of Electronics and Communication Engineering**

Invitation

You are cordially invited for the inaugural function of

*Five days National level Workshop
On*

"DSP and Image Processing through Machine Learning"

On 8th January 2019

- Chief Guest** : **Dr. Ashok Rao**
Former Head, Network Project,
CEDT, IISc, Bangalore
- Guest of Honor** : **Sri. S.N. Nagaraja**
Secretary, NES, Shimoga
- Presided by** : **Dr. H. R. Mahadevaswamy**
Principal, JNNCE
- Presence** : **Dr.L.K Sreepati**
Vice-Principal, JNNCE
- Convener** : **Dr. P.Manjunatha**
Professor and HOD, Dept. of E&C

Date: 08.01.2019

Time: 9.00 A.M.

Venue: AD Block Seminar Hall
Welcome to All

HOD and Staff
Department of Electronics and Communication Engineering



National Education Society(R.)

Jawaharlal Nehru National College of Engineering,
Shivamogga



Internal Quality Assurance Cell(IQAC)

Five days National level Workshop

On

"DSP and Image Processing through Machine Learning"

8th to 12th January 2019

A Report

The current trend of research demands sound knowledge on Machine Learning and its implementation. A workshop covering the basics of machine learning was formulated under the guidance of Dr H R Mahadevaswamy, Principal JNNCE. The department of Electronics and Communication has organized a five days National Level Workshop on DSP and Image processing through Machine learning from 8th to 12th of January 2019. Dr.Manjunatha P, HOD, Department of ECE is the workshop Convener and the co-conveners of the workshop are Roopa B S Assistant Professor Department of ECE, Prema K N Assistant Professor Department of ECE and Smitha S M Assistant Professor, Department of ECE. About 60 participants registered for the workshop from various engineering colleges, research scholars from Kuvempu university, faculty from DSDNP polytechnic and inhouse faculty. The workshop was inaugurated on 8th January by Dr.Ashok Rao, Former Head Network Project, CEDIT, IISC, Bangalore, Sri S.N. Nagaraj, Secretary N.E.S, Dr Dr H R Mahadevaswamy Principal JNNCE and Dr L K Sripathi Vice Principal JNNCE. Dr Ashok Rao in his inaugural speech, spoke about the need of machine learning and data analysis in various applications.

The first day of the workshop was handled by Dr Ashok Rao. He gave a deep insight about the basics of machine learning. He also highlighted the mathematical background and programming tools required for implementing the data science and Machine learning. The second day was completely dedicated to hands-on session on Linux and Python programming. The morning session was on Linux OS and Mr Chethan K R, Assistant Professor, Department of CSE, explained the basics of the Linux platform and gave a detailed hands-on presentation on relevant and necessary Linux commands. The post lunch session was delivered by Mr. Vinay and Mr. Sathvik, from Juggle Enterprice Private Limited, Mysore. They gave an overview of python and explained how to develop simple programs using python.

On the third day, Dr Arulalan Rajan from NITK Surathkhal, gave an exhaustive explanation on Linear algebra and its applications in the forenoon session and was followed by the session on probability theory and random processes. The fourth day was handled by Dr Mohamed Imran, Ejyle Technologies (P) Ltd, Bangalore. He covered the theory on Machine Learning using regression, classification and CNN based deep learning using MatLab. The afternoon session was about Demonstration of Matlab programs. On the last day Dr.Deepu vijayasenan, from NITK Surathkhal, gave a detailed lecture and Demonstration on Machine Learning for speech signal processing using Keras Software.

Dr.P.Manjunatha

Convener & HOD, Dept. of ECE

Head of the Department

Electronics and Communication

J.N.N. College of Engineering

SHIMOGA-577 204.

Dr.H R Mahadevaswamy

Principal, JNNCE
PRINCIPAL

Jawaharlal Nehru

National College of Engineering

SHIMOGA - 577 204



Five days National Level Workshop on "DSP and Image Processing through Machine Learning"

Registration Details

Date: 08.01.2019

Sl.No	Name	College	Contact No	Email_ID
01	Hemanthakumar, R.k	BETM, Ballari	7892786448	hemanthakumar@bitm.edu.in
02	B.S. UJWALA	JNCE, Shivmoga	9886537121	ujwalasarvi2004@jncc.ac.in
03	Rohini H.M	RVMCE Ballari	9902502026	rohinihalakatti@gmail.com
04	Radhika S.K	DSDN polytechnic	8123001535	bashtikm.shivmoga@gmail.com
05	Advi B.V	DSDN.P	9480290161	advibv@gmail.com
06	Hema H.P	DSDNP	9591363419	hemahp4@gmail.com
07	Kavya S	DSDNP	8105278143	kavya26feb@gmail.com
08	Navyashree S.S	DSDNP	7795148803	navya.ss16@gmail.com
09	Pradeepa S.C	JNCE, Smg	9036496015	pradeepa-sc@jncc.ac.in
10	Abhijith N	JNCE	9620852495	abhijith@jncc.ac.in
11	Sushma U.K	JNCE DSDNP	9739138027	sushmauk8@gmail.com
12	Gangambika P.B	DSDNP	9611147607	ganga.prash@gmail.com
13	Chidambara B	DSDNP/EC	9844474430	chidu.b@gmail.com
14	Shwetha B	JNCE, Shivmoga	9886473655	Shwethab@jncc.ac.in
15	G.C. Kamalame	JNCE Shivmoga	—	gckamalame@jncc.ac.in
16	Shwetha H.R	JNCE, Smg	7206663953	shwethahr@jncc.ac.in
17	Aparna	JNCE, Shivmoga	9844627166	apamaraj@jncc.ac.in
18	Sunil M.D.	JNCE, Smg	9886503242	mdsunil.dev@jncc.ac.in

polytechnic



Five days National Level Workshop on "DSP and Image Processing through Machine Learning"

Registration Details

Date: 08.01.2019

Sl.No	Name	College	Contact No	Email_ID
37	Sandeep	Kuvempu Univ	9591456517	Sandeepgonda33@proton.com
38	VIDYASAGAR S.D.	KUVEMPU UNIVERSITY	9844012457	VIDYASAGARSD@GMAIL.COM
39	Madhusudhan S	Kuvempu Univ	9480544177	madhu.hsd@gmail.com
40	Srinivasa S.R	Kuvempu Univ	9164068447	Srinivasa 1991.sri@ghai.in
41	Prashantha M.C.	Kuvempu	8105826908	prashantha.M.C.87@gmail.com
42	Raghu Kumar D.C	Kuvempu	8971345112	rag12clk@gmail.com
43	Siddhant	-	8095232412	g. shivakumar ece@gmail.com
44	Amani Ali Ahmed Ali	Kuvempu	7795481049	dramanializ@gmail.com
45	Kiptanui Linus	Kuvempu	9591920430	linusunguli@gmail.com
46	SUDHEESH K.V	VVCE, Mysuru	9945765761	sudheesh.kv@vvce.ac.in
47	JAGADEESH B	VVCE, Mysuru	9844586127	jagadeesh.b@vvce.ac.in
48	PRAVEENA K.S	VVCE, Mysuru	8970787292	praveena.ks@vvce.ac.in
49	Navya	Kuvempu		
50	Divya	-		
51	Sheela S.	JNCE		
52	RAMESH K	UBDTCE	9964869945	rammybdt@gmail.com
53	Dr. Manjanna N	UBDTCE	9740524245	manjubdt2009@gmail.com
54	Vidhyabharati H.B	JNCE	9986968278	vidhyabharati453@gmail.com
55	Nikitha N.L	JNCE	9090571414	nikitha.n96@gmail.com
56	Ruchika E	JNCE	9591740687	ruchikac987@gmail.com
57				

university

THE NEW
**INDIAN
EXPRESS**

TUESDAY 08.01.2019

JNCE is organising a five-day national seminar on DSP and image processing through machine learning

Where: Administrative block auditorium at the college

When: Today at 9 am

**ಜನ ಹೋರಾಟ
ಮಂಗಳವಾರ-08, ಜನವರಿ 2019
ಜೆಎನ್‌ಎನ್‌ಸಿಇಯಲ್ಲಿ
ರಾಷ್ಟ್ರೀಯ ಕಾರ್ಯಾಗಾರ**

ಶಿವಮೊಗ್ಗ: ಜೆಎನ್‌ಎನ್‌ಸಿಇ ಕಾಲೇಜಿನ ಇಂಜಿನಿಯರಿಂಗ್ ವಿಭಾಗ ಮತ್ತು ಐಕ್ಯೋಎಸಿ, ಐಇಟಿಇ ಹಾಗೂ ಐಎಸ್‌ಟಿಇ ಇವರುಗಳ ಸಂಯುಕ್ತಾಶ್ರಯದಲ್ಲಿ ಮಷಿನ್ ಲರ್ನಿಂಗ್ ಮೂಲಕ ಡಿಎಸ್ ಮತ್ತು ಇಮೇಜ್ ಎಂಬ ವಿಷಯ ಕುರಿತು 5 ದಿನಗಳ ರಾಷ್ಟ್ರೀಯ ಕಾರ್ಯಾಗಾರವನ್ನು ಕಾಲೇಜಿನ ಆಡಳಿತ ವಿಭಾಗದ ಸಭಾಂಗಣದಲ್ಲಿ ಜ.8 ರಿಂದ 12ರವರೆಗೆ ಏರ್ಪಡಿಸಲಾಗಿದೆ. ಕಾಲೇಜಿನ ಪ್ರಾಂಶುಪಾಲ ಡಾ. ಹೆಚ್.ಆರ್. ಮಹಾದೇವಸ್ವಾಮಿ ಕಾರ್ಯಕ್ರಮದ ಅಧ್ಯಕ್ಷತೆ ವಹಿಸಲಿದ್ದು, ಉಪ ಪ್ರಾಂಶುಪಾಲ ಡಾ. ಎಲ್.ಕೆ. ಶ್ರೀಪತಿ, ಇ ಮತ್ತು ಸಿ ವಿಭಾಗದ ಕಾರ್ಯಾಗಾರದ ಸಂಚಾಲಕ ಡಾ.ಪಿ. ಮಂಜುನಾಥ, ನೆಟ್ ವರ್ಕ್ ಯೋಜನೆ ಮುಖ್ಯಸ್ಥ ಡಾ. ಅಶೋಕ್, ರಾಷ್ಟ್ರೀಯ ಶಿಕ್ಷಣ ಸಮಿತಿ ಕಾರ್ಯದರ್ಶಿ ಎಸ್. ಎನ್. ನಾಗರಾಜ ಮತ್ತಿತರರು ಕಾರ್ಯಾಗಾರದಲ್ಲಿ ಪಾಲ್ಗೊಳ್ಳುವರು ಎಂದು ಪ್ರಾಂಶುಪಾಲರು ತಿಳಿಸಿದ್ದಾರೆ.

ವಿಜಯ ಕರ್ನಾಟಕ ಸುದ್ದಿ, 9 ಜನವರಿ 2019.

ಜೆಎನ್‌ಎನ್‌ಸಿಇ ಕಾಲೇಜಿನಲ್ಲಿ ರಾಷ್ಟ್ರೀಯ ಕಾರ್ಯಾಗಾರ

ಜ್ಞಾನ ಸದ್ಭಳಕೆಯಲ್ಲಿ ಉದ್ಯೋಗ ಸೃಷ್ಟಿ ಅಡಕ

• ಡಿ.ಎಸ್.ಎಸ್.ಎಸ್.ಎಸ್.

ಕಾರ್ಯಾಗಾರದ ಮೊದಲ ದಿನದ ಕಾರ್ಯಕ್ರಮದ ಉದ್ಘಾಟನೆ ನಡೆಯಿತು. ಈ ಸಂದರ್ಭದಲ್ಲಿ ಕಾಲೇಜಿನ ಪ್ರಾಂಶುಪಾಲರು ಮಾತನಾಡಿದರು.



ಕಾರ್ಯಾಗಾರದ ಮೊದಲ ದಿನದ ಕಾರ್ಯಕ್ರಮದ ಉದ್ಘಾಟನೆ ನಡೆಯಿತು. ಈ ಸಂದರ್ಭದಲ್ಲಿ ಕಾಲೇಜಿನ ಪ್ರಾಂಶುಪಾಲರು ಮಾತನಾಡಿದರು.

ಕಾರ್ಯಾಗಾರದ ಮೊದಲ ದಿನದ ಕಾರ್ಯಕ್ರಮದ ಉದ್ಘಾಟನೆ ನಡೆಯಿತು. ಈ ಸಂದರ್ಭದಲ್ಲಿ ಕಾಲೇಜಿನ ಪ್ರಾಂಶುಪಾಲರು ಮಾತನಾಡಿದರು.

ವಿಜಯವಾಣಿ

ಮಂಗಳವಾರ 8 ಜನವರಿ 2019

ಡಿಎಸ್‌ಪಿ ಮತ್ತು ಇಮೇಜ್ ಪ್ರಾಸೆಸಿಂಗ್ ವಿಚಾರ ಸಂಕಿರಣ ಉದ್ಘಾಟನೆ: ಐಐಎಸ್‌ಸಿ ನೆಟ್ ವರ್ಕ್ ಪ್ರಾಜೆಕ್ಟ್ ಮಾಜಿ ಮುಖ್ಯಸ್ಥ ಡಾ. ಅಶೋಕ್ ರಾವ್, ಅತಿಥಿ: ಎನ್‌ಇಎಸ್ ಕಾರ್ಯದರ್ಶಿ ಎಸ್. ಎನ್. ನಾಗರಾಜ್, ಅಧ್ಯಕ್ಷತೆ: ಜೆಎನ್‌ಎನ್‌ಸಿಇ ಪ್ರಾಚಾರ್ಯ ಡಾ. ಎಚ್.ಆರ್. ಮಹಾದೇವಸ್ವಾಮಿ, ಸ್ಥಳ: ಜೆಎನ್‌ಎನ್‌ಸಿಇ ಎಂಬಿಎ ಸಭಾಂಗಣ, ಬೆಳಿಗ್ಗೆ 9ಕ್ಕೆ

National Education Society(R.)

Jawaharlal Nehru National College of Engineering,
Shivamogga

Internal Quality Assurance Cell(IQAC)
Five days National level Workshop

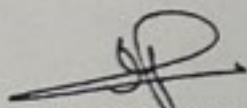
On
"DSP and Image Processing through Machine Learning"
8th to 12th January 2019

A Report

The current trend of research demands sound knowledge on Machine Learning and its implementation. A workshop covering the basics of machine learning was formulated under the guidance of Dr H R Mahadevaswamy, Principal JNNCE. The department of Electronics and Communication has organized a five days National Level Workshop on DSP and Image processing through Machine learning from 8th to 12th of January 2019. Dr.Manjunatha P, HOD, Department of ECE is the workshop Convener and the co-conveners of the workshop are Roopa B S Assistant Professor Department of ECE, Prema K N Assistant Professor Department of ECE and Smitha S M Assistant Professor, Department of ECE. About 60 participants registered for the workshop from various engineering colleges, research scholars from Kuvempu university, faculty from DSDNP polytechnic and inhouse faculty. The workshop was inaugurated on 8th January by Dr.Ashok Rao, Former Head Network Project, CEDIT, IISC, Bangalore, Sri S.N. Nagaraj, Secretary N.E.S, Dr Dr H R Mahadevaswamy Principal JNNCE and Dr L K Sripati Vice Principal JNNCE. Dr Ashok Rao in his inaugural speech, spoke about the need of machine learning and data analysis in various applications.

The first day of the workshop was handled by Dr Ashok Rao. He gave a deep insight about the basics of machine learning. He also highlighted the mathematical background and programming tools required for implementing the data science and Machine learning. The second day was completely dedicated to hands-on session on Linux and Python programming. The morning session was on Linux OS and Mr Chethan K R, Assistant Professor, Department of CSE, explained the basics of the Linux platform and gave a detailed hands-on presentation on relevant and necessary Linux commands. The post lunch session was delivered by Mr. Vinay and Mr. Sathvik, from Juggle Enterprice Private Limited, Mysore. They gave an overview of python and explained how to develop simple programs using python.

On the third day, Dr Arulalan Rajan from NITK Surathkhal, gave an exhaustive explanation on Linear algebra and its applications in the forenoon session and was followed by the session on probability theory and random processes. The fourth day was handled by Dr Mohamed Imran, Ejyle Technologies (P) Ltd, Bangalore. He covered the theory on Machine Learning using regression, classification and CNN based deep learning using MatLab. The afternoon session was about Demonstration of Matlab programs. On the last day Dr.Deepu vijayasenan, from NITK Surathkhal, gave a detailed lecture and Demonstration on Machine Learning for speech signal processing using Keras Software.



Dr.P.Manjunatha

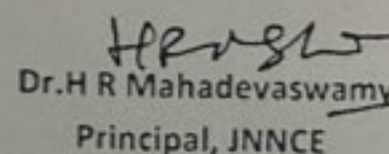
Convener & HOD, Dept. of ECE

Head of the Department

Electronics and Communication

J.N.N. College of Engineering

SHIMOGA-577 204.



Dr.H R Mahadevaswamy
Principal, JNNCE