

## Registration Form

Student Development Programme  
On

**INTRODUCTION TO BASIC ELECTRONICS**  
**20/09/2019**

1. Name: \_\_\_\_\_
2. College: \_\_\_\_\_
3. Department: \_\_\_\_\_
4. Year/Sem: \_\_\_\_\_
5. Email-ID: \_\_\_\_\_
6. Mobile No: \_\_\_\_\_

The information provided by me is true to the best of my knowledge. I agree to abide by the rules and regulations governing the Student Development Programme. If selected, I shall attend the program for the entire duration.

Date :

Place :

Signature of the Applicant

## Organizing Committee

Dr. H. R. Mahadevaswamy, Principal

Dr. P. Manjunatha, HoD E&C Department

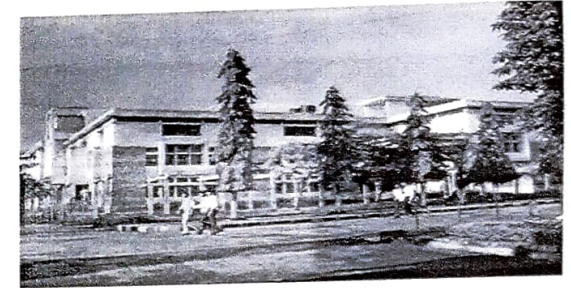
Mrs. Prema K N, Asst Prof, ECE Department

## Student Coordinators

1. Shripada Adiga
2. Siddarth Muchadi
3. Sumanth Shanbog H R
4. Suhas N S
5. Yashas Vinay
6. Shshank G S
7. Shshank K S
8. Sanath N
9. Shivakumar M
10. Shamantha B R
11. Bhanupriya
12. Priya
13. Priyanka P S

National Education Society ®  
**Jawaharlal Nehru National College of  
Engineering, Shimoga – 577 204**

[Affiliated to VTU Belagavi, Recognized by Govt. of Karnataka, Accredited  
by NAAC and Approved by AICTE, New Delhi]  
Internal Quality Assurance Cell



Student Development Programme  
On

**INTRODUCTION TO BASIC ELECTRONICS**

From

20/09/2019

Organized by

**Department of Electronics & Communication  
Engineering**

**Address for Communication:**

Mrs. Prema K N, Asst Prof, Ph:827732422  
Siddarth muchadi ,student coordinator ph:8105414991

**NATIONAL EDUCATION SOCIETY(R.)**

**JAWAHARALAL NEHARU NATIONAL COLLEGE OF  
ENGINEERING, SHIVAMOGGA.**

**ELECTRONICS AND COMMUNICATION BRANCH .**

**WORKSHOP FOR FIRST YEAR STUDENTS ON BASIC ELECTRONICS(18ELN14/24).**

**FACULTY COORDINATOR :PREMA K N (premakn@jnnce.ac.in)**

**STUDENT COORDINATORS OF THIRD SEMESTER:**

1. Shripada Adiga
2. Siddarth Muchadi
3. Sumanth Shanbog H R
4. Suhas N S
5. Yashas Vinay
6. Shshank G S
7. Shshank K S
8. Sanath N
9. Shivakumar M
10. Shamantha B R
11. Bhanupriya
12. Priya
13. Priyanka P S

**TOPICS**

**THEORY PRESENTATIONS :**

1. SEMICONDUCTOR DIODE AND ITS APPLICATIONS.
2. FET, SCR AND CMOS.
3. OPERATIONAL AMPLIFIER AND ITS APPLICATIONS.
4. BJT APPLICATIONS FEEDBACK AMPLIFIERS AND OSCILLATORS.
5. DIGITAL ELECTRONICS FUNDAMENTALS.

**LAB MODELS AND HANDSON:**

1. RECTIFIER CIRCUITS (HALF AND FULL WAVE RECTIFIER WITH OUT FILTER.).
2. JFET CIRCUITS.
3. OP-AMPS (VOLTAGE FOLLOWER AND INVERTING AMPLIFIER )
4. TRANSISTOR AS SWITCH CIRCUITS.
5. TESTING OF IC (ALL BASIC GATES AND UNIVERSAL GATES. )

Jawaharlal Nehru National College of Engineering, Shimoga  
Department of Electronics and communication Engineering

Date: 20.09.2019

Basic Electronics Work Shop

I SEM. B.E 2019-20

BATCH No.G - 1

SL. No.	USN	STUDENT NAME	SIGNATURE
1.	ECE	DARSHITH S	
2.	ECE	DHANANJAYA K S	
3.	ECE	DIVYASHREE H N	Divyashree H.N
4.	ECE	ESHANYA Y N	Eshanya
5.	ECE	GAGAN K R	
6.	ECE	GOWRI G BENNUR	
7.	ECE	GOWTHAM N M	
8.	ECE	HARINI NANDA B.L	Harini Nanda
9.	ECE	HARSHA K S	
10.	ECE	HARSHITHA L	
11.	ECE	HRUTHIK B C	
12.	ECE	HRUTHIKA G	
13.	ECE	INCHARA P	
14.	ECE	INCHARA SANNAKKI K P	Inchara K.P
15.	ECE	JAHNAVI K	Jahnavi K
16.	ECE	JEEVITHA H C	
17.	ECE	KAMMACHI PUTTASWAMY VINDHYA	K.P. Vindhya
18.	ECE	KAREGOWDRA DIVYA	
19.	ECE	KISHAN S N	
20.	ECE	KOUSHIK S K	
21.	ECE	KUSUMA P	
22.	ECE	LATHESHA V	
23.	ECE	M R JAIVIKA	Jaivika
24.	ECE	MADHURA VISHWANATH	
25.	ECE	MAHENDRA H H	
26.	ECE	MANJUNATH P PATIL	
27.	ECE	MARIA MONICA P	
28.	ECE	MEGHA B	
29.	ECE	NAGESHA M	Nagesha M
30.	ECE	Neha K M	NEHA K.M
31.	ISE	DHANUSH NAVUDA K R	Dhanush Navuda K.P
32.	ISE	HARINI M	Harini M
33.	ISE	HARSHITHA G P	
34.	ISE	KARTHIK JADHAV R P	
35.	ISE	KAVYA G S	Kavya G.S
36.	ISE	KIRAN M N	
37.	ISE	LAVANYA A S	
38.	ISE	LIKITH L S	
39.	ISE	MANASA B R PATIL	Manasa B.R
40.	ISE	MANOJ T J	
41.	ISE	MAYANK KUMAR	Mayank Kumar
42.	ISE	MEDHA M	

