SDP on analog & di

DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING
JNN COLLEGE OF ENGINEERING, SHIMOGA 577204

Date :18-10-2021

Circular

We are organizing Student Development Program on "Developing Analog and Digital circuits" for 3rd Sem students of Electronics and Telecommunication Engineering Department on 25-10-2021 and 26-10-2021.

All the students are requested to attend the same.

Coordinators
Mrs. Aparna
Mrs. Rashmi M Hullamani Rashmi

18/10/2021
HOD, E&TE Dept.,

National Education Society®

J N N College of Engineering,
Shimoga

Department of Electronics & Telecommunication Engineering

In Association with TELEGMA

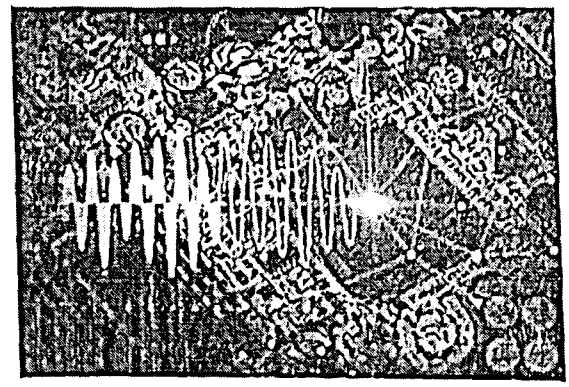
Invitation

You are cordially invited to attend Two Days SDP on

"Developing Analog and Digital Circuits"

On

25th and 26th October 2021@9:30am



***Welcome you all**

Coordinators
Mrs. Aparna
Mrs. Rashmi M Hullamani

HOD and Staff
Dept of E&TE. JNNCE

National Education Society (R.)

Jawaharlal Nehru National College of Engineering, Shivamogga

Department of Electronics & Telecommunication Engineering

SDP on Developing Analog and Digital Circuits

Attendance

Sl No	USN	NAME	25/10/2021	26/10/2021
1	4JN20ET001	A M MALLIKARJUNA		
2	4JN20ET002	ADITI S G		
3	4JN20ET003	AFHAM ALI BEIG		
4	4JN20ET004	ANANYA S P		
5	4JN20ET005	ANKITHA V		
6	4JN20ET006	BINDU G P		
7	4JN20ET007	DHANYA		
8	4JN20ET008	DHARITHRI H L		
9	4JN20ET009	HRUTHIK A B		
10	4JN20ET010	INCHARA J		
11	4JN20ET011	JEEVAN S R		
12	4JN20ET012	KOUSTUBHA HEGDE		
13	4JN20ET013	MAHEK ANJUM RIYAZ SHAIKH		
14	4JN20ET014	MEGHANA PRABHAKARA RAO		
15	4JN20ET015	MOHAMMED AFROZ AHMED		
16	4JN20ET016	NAJMUSSAHER		
17	4JN20ET017	NISCHITHA S R		
18	4JN20ET018	PRANAVA SWAROOPA K S		
19	4JN20ET019	PRIYANKA J		
20	4JN20ET020	PRIYANKA N		
21	4JN20ET021	RAKSHITHA S J		
22	4JN20ET022	ROHINI H N		
23	4JN20ET023	SAISHREE P		
24	4JN20ET024	SANNIDHI T N		
25	4JN20ET025	SHAMITA NARASIMHA KAMAT		
26	4JN20ET026	SHIFANA FATHIMA		
27	4JN20ET027	SHREE LAKSHMI K S		
28	4JN20ET028	SNEHA M S		
29	4JN20ET029	SRINIVAS S		
30	4JN20ET030	SUBHASH CHANDRA		
31	4JN20ET031	SUDARSHAN T B RAMANA		
32	4JN20ET032	TEJASHWINI D		
33	4JN20ET033	VARUNA H V		
34	4JN20ET034	VIDHYUN R		
35	4JN20ET035	VISMITHA V R		
36	4JN20ET036	VYSHNAVI S GUJJAR		

Rashmi

HoD 18/10/2021

Report of One Day SDP on "Developing Analog and Digital Circuits" on 25-10-2021 and 26-10-2021

Organising Chair: Dr. Ushadevi M. B.
HoD, Dept. of ETE,
JNNCE, Shivamogga.

Co-ordinator: Mrs. Aparna
Assistant Professor,
Dept. of ETE, JNNCE, Shivamogga.

Mrs. Rashmi M Hullamani
Assistant Professor
Dept. of ETE, JNNCE, Shivamogga.

Course Outcomes:

At the end of the SDP the students will be able to:

CO1	Understand the basics of Analog and Digital Circuits	PO1,PO2
CO2	Implement simple Analog and Digital Circuits	PO1,PO2,PO3,PO5,PO9,PO10
CO3	Demonstrate better soft and Life Skills	PO10,PO12

Report:

One Day SDP on "Developing Analog and Digital Circuits" was organized by Electronics and Telecommunication department for 3rd semester students on 25-10-2021 and 26-10-2021

Students from 3rd semester were enrolled for the SDP and proactively participated in the SDP. The SDP was stated with introduction to basics of analog and digital circuits followed by hands on.

The following were hand on covered in SDP

1. To study the basics of digital circuits.

2. Testing of IC using IC tester Kit.
3. Verification of logic gates.
4. Simplification and Realization of Boolean expression using logic gates/Universal gates.
5. How to rig up circuits in bread board, Usage of Multimeter and Digital meter.
6. Functions of CRO and AFO.
7. How to generate Sine wave in CRO?
8. To switch on/off an LED using diode in forward /reverse bias.
9. Transistor switch circuits to operate a relay that switched off/on an LED.
10. +5v power supply unit using bridge rectifier, capacitor filter and IC 7805

Conclusion:

The overall SDP was more benefited to the students. Students learned about electronics devices and were able to a communicate effectively, able to engage independently and learn to work in a team.