



NATIONAL EDUCATION SOCIETY (R.)  
**J N N COLLEGE OF ENGINEERING, SHIVAMOGGA**  
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**Department of Computer Science & Engineering**  
Internal Quality Assurance Cell

**SHORT TERM STUDENT TRAINING PROGRAM**  
on  
**ARM Microcontroller**



Resource Person  
**Mr. Harish V Mekali**  
Asst. Professor,  
BMSCE, Bengaluru

About this STTP: The ARM processor core is a key component of many successful embedded systems. ARM cores are widely used in mobile phones, handheld organizers, and a multitude of other everyday portable consumer devices.

During this STTP, student will learn:

- The hardware fundamentals of ARM processor and its architecture
- The ARM instruction set

**Dr. K.M. Poornima**  
HoD, CS&E,  
JNNCE

**Dr. Manjunatha P.**  
Principal,  
JNNCE

**Sri. S. N. Nagaraja**  
Secretary,  
NES, Shivamogga

Coordinators:  
**Pushpa R N**  
Asst. Professor,  
CS&E, JNNCE  
**Nandish M**  
Asst. Professor,  
CS&E, JNNCE

Date: 11th June to 14th June 2021

Time: 11.30- 1.30

Inauguration: 11<sup>th</sup> June 2021 at 11.30 AM

NES Management, Principal, Faculty, Staff  
Dept. of CS&E, JNNCE, Shivamogga

<http://meet.google.com/nwg-aabg-ibk>





**J. N. N College of Engineering, Shivamogga**  
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 All 7 UG Programs have been Accredited By National Board Of Accreditation (NBA)  
 for period 1.7.2019 to 30.6.2022,  
 Recognized by Govt. of Karnataka and Affiliated to VTU, Belagavi)

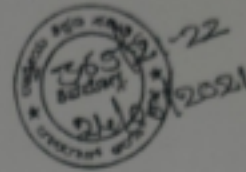


17

Date: 07-06-2021

To,  
 The Principal  
 JNNCE, Shivamogga

From,  
 Mr. Nandish M & Mrs. Pushpa RN  
 Asst. Professor, Dept. of C.S.E  
 JNNCE, Shivamogga



Through,  
 Head of the Department  
 Dept. of C.S.E  
 JNNCE, Shivamogga

*NO-NES/JNNCE/45/2021 D:22/6/21*

*Forwarded to Secretary  
 for your kind approval*

Dear Sir,

**Subject: Permission request for Organizing 3 day STTP on "ARM MICROCONTROLLER"**

*Principal  
 Jawahar Nehru New  
 College of Engineering (JNNCE)  
 Shivamogga.*

We are writing to request your permission for a training event on "ARM MICROCONTROLLER". Resource person Mr. Harish V Mekali, Assistant Professor, BMSCE Bangalore, would like to speak to students about ARM microcontroller architecture and instruction sets. Details are listed below.

- Location: Google Meet -Online
- Time & Date: 11:30 am - 01:30 pm, 11-14, June 2021
- Resource Person: Educational speaker, Mr. Harish V Mekali
- Audience expected: 4<sup>th</sup> sem CSE, Students, approx. 130

Thank you very much for your time and consideration of my request.

*Forwarded for the needful*

*21/06/21*  
 Sincerely,  
 Mr. Nandish M & Mrs. Pushpa RN

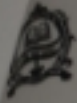
*24/6/2021*  
 HPT  
 Secretary

*Submitted for approval*

*24/06/2021*

*CSE/ISS/SSO*

National Education Society  
 Shivamogga



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Date: 07-06-2021

To,  
The Principal  
JNNCE, Shimoga

From,  
Mr. Nandish M & Mrs. Pushpa RN  
Asst. Professor, Dept. of C.S.E  
JNNCE, Shimoga

Through,  
Head of the Department  
Dept. of C.S.E  
JNNCE, Shimoga

Dear Sir,

**Subject: Requesting to sanction remuneration to resource person for 3 day STTP on ARM MICROCONTROLLER.**

With reference to the above subject, we are requesting you to sanction amount Rs. 5000 for resource person Mr. Harish V Mekali, Assistant Professor, BMSCE Bangalore. Details of the Event are listed below.

- Location: Google Meet -Online
- Time & Date: 11:30 am – 01:30 pm, 11-14, June 2021
- Resource Person: Educational speaker, Mr. Harish V Mekali
- Audience expected: 4<sup>th</sup>sem CSE, Students, approx. 130

We have attached a copy of the brochure. Thank you very much for your time and consideration of my request.

*Forwarded for the needful*  
*shd*

Sincerely,  
*Pushpa*  
Mr. Nandish M & Mrs. Pushpa RN



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**J N N College of Engineering, Shivamogga**

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**Department of Computer Science and Engineering**  
**Short Term Student Training Program on ARM Microcontroller**

*Certificate*

*This is to certify that Spoorthi D A attended three days Short term student program on "ARM Microcontroller" organized by the Department of CS&E, JNNCE, Shivamogga. on 11th to 14th June 2021*

**NANDISH M**  
Coordinator, & Asst. Prof., CSE

**PUSHPA R N**  
Coordinator & Asst. Prof., CSE

**Dr. Poornima K.M**  
HOD, Dept. of CS&E, JNNCE

**Dr. P. Manjunatha**  
Principal, JNNCE

# Feedback Form on "ARM Microcontroller" STTP Day 1

116 responses

Publish analytics

## USN

116 responses

4JN19CS070

4JN19CS124

4JN18CS090

4JN19CS036

4JN19CS107

4jn19cs095

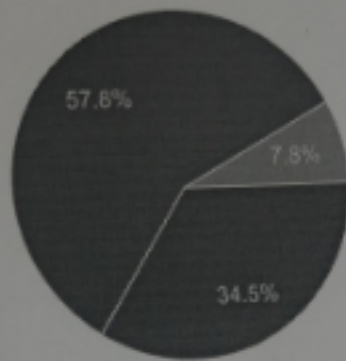
4JN19CS085

4JN19CS069

4JN19CS017

### How informative did you find STTP Session 1?

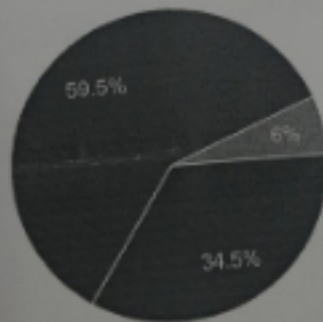
116 responses



- Extremely informative
- Very informative
- Moderately informative
- Not very informative

### How clear were the ideas and concepts were presented?

116 responses



- Extremely clear
- Very clear
- Moderately clear
- Not very clear

### The Trainer were Knowledgeable

115 responses

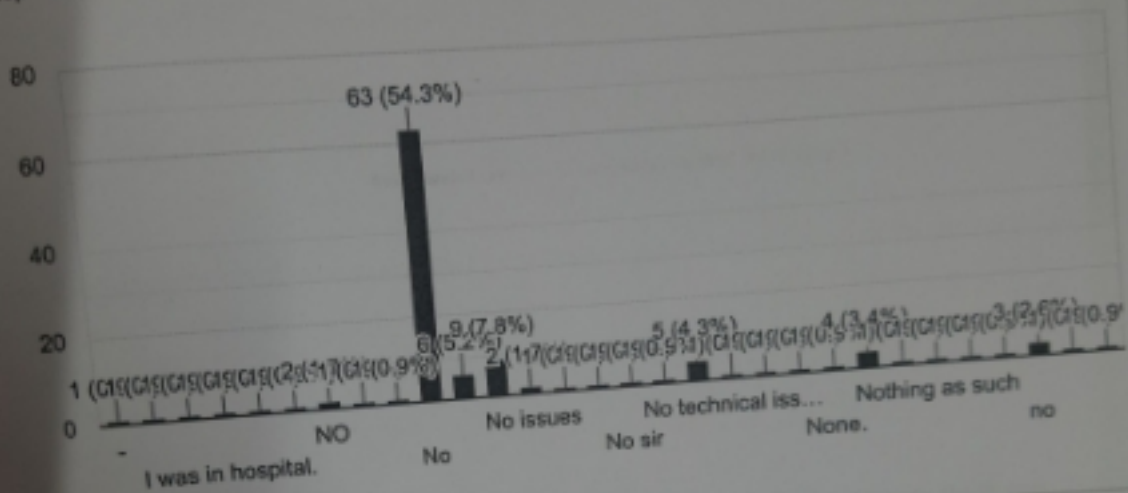


- Strongly disagree
- Disagree
- Neutral
- Agree
- Strongly agree

Feedback Form on "ARM Microcontroller" STTP Day 1

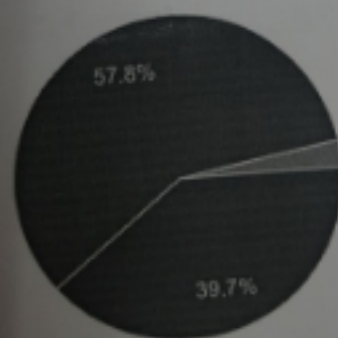
Were there any technical issues that prevented you from seeing or hearing?  
If so, what were they?

116 responses



Overall how satisfied were you with this STTP Day 1?

116 responses



- Very Satisfied
- Satisfied
- Neither satisfied nor unsatisfied
- Dissatisfied

Do you have any other comments or feedback?

55 responses

No

No

no

no comments

Was very good

No, it's fyn

The entire session was beautifully carried out by Harish sir..He just made us think out of the boundaries.....Do conduct more sessions like this...

Ntg

This was too informative.



Are there any topics you'd like to see covered in future STTP's?

37 responses

No

Yes

no

Let's see

.

Cloud computing

Any other kind of processor to discuss and their difference

Satisfied

Topics and fundamentals on Design and analysis of algorithms

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Google Forms

# Feedback Form on "ARM Microcontroller" STTP Day 2

104 responses

Publish analytics

## USN

101 responses

4JN19CS020

4JN19CS081

4JN19CS026

4JN19CS019

4JN19CS022

4JN19CS047

4JN19CS060

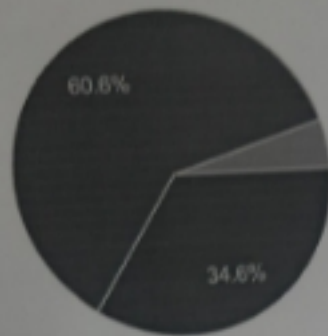
4jn19cs123

4JN19CS003

4JN19CS006

How informative did you find STTP day 2?

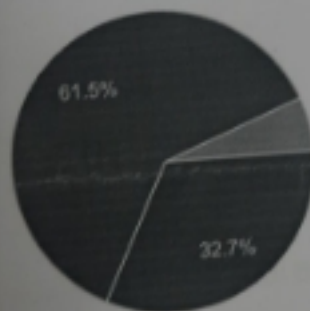
104 responses



- Extremely informative
- Very informative
- Moderately informative
- Not very informative

How clear were the ideas and concepts were presented?

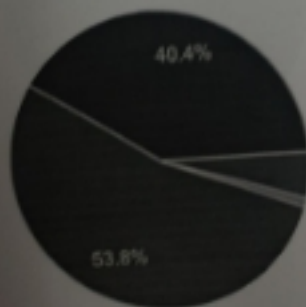
104 responses



- Extremely clear
- Very clear
- Moderately clear
- Not very clear

The Trainer were Knowledgeable

104 responses



- Strongly disagree
- Disagree
- Neutral
- Agree
- Strongly agree

Do you have any other comments or feedback?

40 responses

No

Nothing

Nope

no

Sir/Ma'am ,the sttp was really helpful..  
please make us get more sttp s on different topics like operating system, data  
communication

All the concepts were clear... Will be Waiting for more sessions from sir....

It was really good

It was great session

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Google Forms



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6.7.2021

**Department of Computer Science and Engineering**

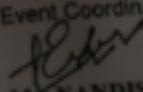
**Proceedings of 3 days STTP on "ARM MICROCONTROLLER"**

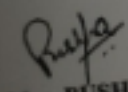
A STTP titled 'ARM MICROCONTROLLER', was organized by the CSE Department for 4<sup>th</sup> Sem Students on 11<sup>th</sup>, 12<sup>th</sup> and 14<sup>th</sup> June 2021. The STTP was attended by more than 120 participants from CSE Department. Master of ceremony for the function was Mrs. Pushpa RN Asst. Professor. The STTP was graced and inaugurated by the presence of our beloved Secretary of NES, Sri S.N.Nagaraja, Dr.Manjunath P, Principal, Dr. M.M.Rajath Hegde, Dean Infrastructure and Dr.Poornima K.M, Head of Department, Dept. of C.S.E. Mr. Harish V Mekali, Assistant Professor, BMSCE, Bangalore was the main Speaker and resource person for the 3 day training programme.


Dr.Poornima K.M. started the event with welcoming all the guests, resource person, teaching, non-teaching staff and all the participants to the inaugural function. Next, Mr. Nandish M. Asst. Professor briefed about the STTP and introduced the resource person to the gathering. Later, Mr. Harish V Mekali shared his few thoughts on JNNCE College and his relationship with JNNCE. NES Secretary S.N.Nagaraja shared his knowledge on ARM microcontroller and its importance in the real world. He also motivated students to take up the projects related to ARM microcontroller which will be helpful in their career. Lastly, Dr.Jalesh Kumar, Professor, Dept. of CSE, thanked all the guests, resource person, teaching, non-teaching staff and all the participants who have attended the inaugural function.

On Day 1, Mr.Harish V Mekali has discussed on CPU architecture, basics of embedded system, ARM Design philosophy and ARM Processor family. He also discussed different bus technology, Peripherals and computer memory hierarchy. On Day 2, session was started with introduction of ARM architecture and then Data flow model, Pipelining and Register organization were discussed. On Day 3, different processor modes are discussed in detail with examples. Session was ended with discussing different exceptions and Interrupts.

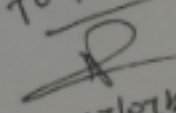
Event Coordinators:

  
MR. NANDISH M  
Asst. Professor  
DEPT. OF CSE

  
Mrs. PUSHPA R.N.  
Asst. Professor  
DEPT. OF CSE

  
Dr.Poornima K.M  
Prof & Head  
DEPT. OF CSE

TO AK

  
02/07/21



National Education Society\*  
**JAWAHARLAL NEHRU NATIONAL COLLEGE OF ENGINEERING**  
**DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING**



**SHORT TERM STUDENT TRAINING PROGRAM on ARM Microcontroller**  
**(11th to 14th June 2021)**

**Honorarium for Resource person**

Name	Harish V Mekali
Designation	Asst. Professor
Institution/Organization	BMS College of Engineering, Bengaluru
Email id	harishmekali@gmail.com
Phone Number	95387 65141
S/B Account No.	50100248485512
IFSC	HDFC0000446
Bank name/ Branch	Basavanagudi, Gandhi Bazar, Bangalore

National Education Society(R.)  
**JAWAHARLAL NEHRU NATIONAL COLLEGE OF ENGINEERING**  
 Navule, SHIVAMOGGA - 577 204, Karnataka.

No. VOUCHER Date: 6.7.2021  
 Received with thanks from THE PRINCIPAL, JNNCE, SHIVAMOGGA, the sum  
 of Rs. 5,000.00 (Rupees Five thousand only)

By Cash/Cheque No.: \_\_\_\_\_ Dated: \_\_\_\_\_  
 towards Honorarium for resource person of STTP on ARM microcontroller  
 for Signature of Receiver

Head of Account: \_\_\_\_\_  
 Name: Mr. Harish V Mekali, Asst. Prof. Dept: BMS College of Engg., Bangalore

due to online STTP  
 voucher is certified  
 by the Co-ordinator.

**"Passed for payment"**  
[Signature]  
**PRINCIPAL**  
 JNNCE, SHIVAMOGGA.

[Signature]  
[Signature]  
 Co-ordinators:

**HOD, CS & E**

**NES - JNNCE**  
 Budget Code 3406  
 Expense Head Conf/wk/FDP/SBP/Scanned  
 Opening Balance \_\_\_\_\_  
 Billing Amount Rs 5000.00  
 Closing Balance \_\_\_\_\_

[Signature]  
**Head**  
[Signature]  
**Principal**