Event Category: Student Development Program

Event Name: "Application Development using

ARM and ATmega Microcontrollers"

Date: 06th August 2021.





National Education Society ® JNN College of Engineering, Shivamogga.



(Approved by AICTE, New Delhi, Certified by UGC 2f & 12B, AccreditedbyNAAC-B,UGprograms:CE,ME,EEE,ECE,CSE,ISE,TCE accredited by NBA:1.7.2019 to30.6.2022, Recognized by Govt. of Karnataka and Affiliated to VTU, Belagavi)

Department of Information Science and Engineering in association with Computer Society of India.



You are all cordially invited for the One Day SDP On

"Application development using ARM and ATmega

Microcontrollers"

06th August 2021 from 10:30AM

Presided By

Dr. Manjunatha P

Principal, JNNCE, Shivamogga

Presence

Dr.Rajath Hegde

Infrastructure Dean JNNCE, Shivamogga

Dr. Sanjeev Kunte R

HOD, Dept. of ISE, JNNCE, Shivamogga

Resource Person

Mr. Manjunatha Kamath,

Assistant Professor, Yenepoya
Institute of Technology, Moodabidri

Date:

06-08-2021

HOD and Staff Members of ISE



NES ®, Shimoga

(Appoved by AICTE, New Delhi, Accredited by NAAC-'B', Recognized by Govt.of Karnataka & Affiliated to VTU, Belgavi) JNN College of Engineering, Shivamogga

Department of Information Science & Engineering (Accredited by National Board of Accreditation)



One Day SDP On

Application development using ARM and ATmega Micrcontrollers

Date: 06th August 2021

Organised by

Department of Information Science & Engineering In Association with

Computer Society of India



Time: 10:30AM

Resource Person

Venepoya Institute of Technology, Mr. Manjunatha Kamath Mangalore, Karnataka Assistant Professor, Moodabidri,

Dept. of IS&E, JNNCE, Shimoga

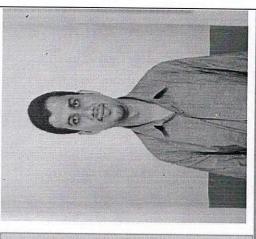
Contact: 9886174174

9620437666

G V Sowmya & Chethan G S

Coordinator

Assistant Professors



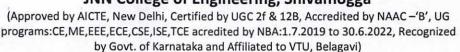
Dr. Manjunatha P

Dr. Sanjeev Kunte R.

Dr. Rajath Hegde



JNN College of Engineering, Shivamogga





Department of Information Science and Engineering

Date: 05-08-2021

To,

The Principal,

JNN College of Engineering,

Shivamogga

Respected Sir,

Sub: Requisition for permission to Conduct one day SDP

We wish to inform you that Department of Information Science and Engineering is planning to conduct online one day SDP on "Application development using ARM and ATmega Microntrollers" on 06/08/2021 from 10.30AM.

Mr. Manjunath Kamath, Assistant Professor, Yenepoya Institute of Technology, Moodabidri is invited as a resource person to this SDP. We request you to kindly permit us to conduct the above SDP

Thanking you,

Yours Sincerely,

HOD, Dept of ISE

Co-ordinators:

G V Sowmya

Chethan G S

Professor and Head

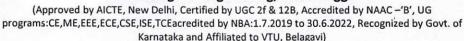
Dept. of Information Science & English

I.N.N. College of Engineering

SHIMOGA-577 201



JNN College of Engineering, Shivamogga





Department of Information Science and Engineering

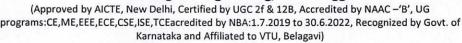
- Presence:Dr.Rajath Hegde, Professor & Dean Infrastructure, JNNCE, Shivamogga
- · Presence: Dr. Sanjeev Kunte R, HOD, ISE department, Shivamogga

On 6thAugust, Friday function started at 10.30 am, Mr. Chethan. G. S, Asst. Prof., ISE department given welcome address to all dignitaries and participants, followed by given a note on about the workshop, thenMrs. G V Sowmya, Asst. Prof., ISE department introduced the resource person Mr.Manjunatha Kamath, Assistant Professor, Yenepoya Instituteof Technology, Moodbidiri for this SDP. After the introduction speech, Dr. Sanjeev Kunte R, HOD, ISE department addressed the guest and participantsand this is followed by, Dr. P Manjunatha, Principal, JNNCE given the presidential address, finally Mrs. G V Sowmya, Asst. Prof., ISE department given the vote of thanks.

A total of 110 attended for this SDP. The participants for this SDP were 4th semester students, IS&E department, JNNCE. Demo is given using Google meeting application.



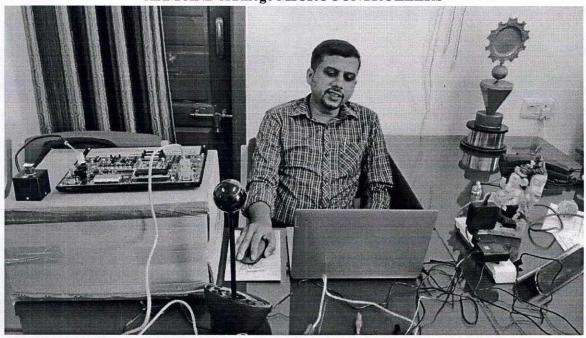
JNN College of Engineering, Shivamogga





Department of Information Science and Engineering

STUDENT DEVELOPMENT PROGRAM ON APPLICATION DEVELOPMENT USING ARM AND ATmege MICROCONTROLLERS



Department of IS & E, JNNCE organized one day STD on "Application development using ARM and ATmega Microcontrollers" on 6th August 2020.

Python is an open source, high-level and powerful programming language that is well suited for scientific computing. The aim of the SDP was to provide hands on training to the students on the use and benefits of ARM and ATmega Microcontroller using Keil IDE software and the following aspects of ARM and ATmega Microcontrollers were covered:

- Introduction to ARM LPC 2148 and Aurdino ATmega Microcontroller.
- Details of ARM Development Board and Aurdino development Board IDE.
- · Robotics ARM application.
- · Demo on Interfacing stepper motor.
- Demo on Smart Gardening application development.

SDP inaugurated on 6th June, Friday, at 10.30 am, through online platform Google meet application in presence of following dignitaries.

- Presided by: Dr. P. Manjunatha, Principal, JNNCE, Shivamogga
- Resource Person: Mr.Manjunatha Kamath, Assistant Professor, Yenepoya Institute of Technology, Moodbidri.



J N N College of Engineering, Shivamogga



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Department of Information Science and Engineering

Feedback Analysis On

Application Development Using ARM and ATmege Microcontrollers

SDP conducted was very helpful to the students to gain the practical knowledge on ARM and ATmega Microcontrollers. Students have given the positive opinion regarding organization of the SDP.

The Content and delivery of the SDP reached to most of the student's expectations. Got the good response from the students regarding talk enhanced technical knowledge in the specified area and also it helped the students to analyze the problem that may arise improved design and development ability.

In SDP real world issues were discussed which was liked by most of students. Newer tools in the industry were demonstrated which helped students to implement the lab programs. Students got interest from this SDP towards implementing the projects on IOT. Finally students also conveyed that they need similar kinds of SDP's with hands on sessions.